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- 1. Specification subject to change without notice.
- 2. All dimensions and specifications apply to standard modules. This information may vary for modules with optional features.
- 3. All dimensions are in millimeters.
- 4. Precautions:These precautions apply equally to modules from all makers, not just Densitron. Violation of these guidelines may void the warranty and can cause problems ranging from erratic operation to catastrophic display failure.

Handling precautions:

• This device is susceptible to Electro-Static Discharge (ESD) damage. Observe Anti-Static precautions.

Power supply precautions:

- Identify and, at all times, observe absolute maximum ratings for both logic and LC drivers. Note that there is some variance between models.
- Prevent the application of reverse polarity to VDD and Vss, however briefly.
- Use a clean power source free from transients. Power up conditions are occasionally "jolting" and may exceed the maximum ratings of the module.
- The +5V power of the module should also supply the power to all devices which may access the display. Don't allow the data bus to be driven when the logic supply to the module is turned off.
- DO NOT install a capacitor between the Vo (contrast) pin and ground. VDD must, at all times, exceed the Vo voltage level. The capacitor combines with the contrast potentiometer to form an R-C network which "holds-up" Vo, at power-down, possibly damaging the module.

Operating precautions:

- DO NOT plug or unplug the module when the system is powered up.
- Minimize the cable length between the module and host MPU. (Recommended max. length 30 cm).
- For models with EL or CCFL backlights, do not disable the backlight by interrupting the HV line. Unloaded inverters produce voltage extremes which may arc within a cable or at the display.
- Operate the module within the limits of the modules temperature specifications.

Mechanical / Environmental precautions:

- Improper soldering is the major cause of module difficulty. Use of flux cleaner is not recommended as they may seep under the elastomeric connection and cause display failure. Densitron recommends the use of Kester "245" no-clean solder.
- Mount the module so that it is free from torque and mechanical stress.
- Surface of LCD panel should not be touched or scratched. The display front surface is an easily scratched, plastic polarizer. Avoid contact and clean only when necessary with soft, absorbent cotton dampened with petroleum benzene.
- ALWAYS employ anti-static procedure while handling the module.
- Prevent moisture build-up upon the module and observe the environmental constraints for storage temperature and humidity.
- DO NOT store in direct sunlight.
- If leakage of the liquid crystal material should occur, avoid contact with this material, particularly ingestion. If the body or clothing becomes contaminated by the liquid crystal material, wash thoroughly with water and soap.

Notes: (unless otherwise specified)

Unless otherwise	APPROVALS	DATE	SANTA FE SPRINGS, CA				
specified: Dimensions are mm	DRAWN						
Tolerances are: $X = \pm 3$ $X = \pm 0.5$	Tolerances are: X = ± 3				240 X 128 GRAPHICS LCD MODULE		
	ISSUED		DWG. NO.	LM3729	SHEET 1 OF 8		

1.0 DESCRIPTION

Dot matrix display module consisting of liquid Crystal Display, CMOS driver and Seiko Epson SED1330 controller LSI, printed circuit board, metal support frame.

Available LC fluid type is: NTN (supertwisted nematic), NTN-H (extended temperature range NTN).

2.0 MECHANICAL CHARACTERISTICS

Item	Specifications	Unit
Package Dimensions	144.0 (W) x 104.0 (H) x 15.6 max (D)	mm
Display format	128 dots (W) x 240 dots (H)	-
Driving method	1/128	duty
Dot size	0.40 (W) x 0.40 (H)	mm
Dot pitch	0.45 (W) x 0.45 (H)	mm
Active display area	107.95 (W) x 57.55 (H)	mm
Viewing area	114.0 (W) x 64.0 (H)	mm
Weight		g

Notes:W-Width;H-Height;D-Depth.

3.0 ABSOLUTE MAXIMUM RATINGS

VSS=0V;Ta=25°C FSTN-H,NTN-H Symbol FSTN,NTN Unit Item Min. Max. Min. Max. Logic supply voltage V 0 0 VDD-VSS 7 7 0 V LC driver supply voltage Vdd-Vo 0 25 25 Operating temperature Тор 0 -20 °C +50 +70 (Note 3) Storage temperature (Note 1) TST -20 +70 -30 +80 Humidity: Operating (@40°C) RH (Note 2) 85% 85% ---Non-operating (@40°C) -95% _ 95% RH (Note 2) -

Notes: 1: Tested to 100 hrs.

2: Refers to non-condensing conditions.

3. It is not recommended to operate CCF lamp below 0°C.

4.0 ELECTRICAL CHARACTERISTICS

VDD=5±0.25V;Ta=25°					5V;Ta=25°C	
Item	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Input "High" voltage	Vih	-	0.8Vdd	-	Vdd	V
Input "Low" voltage	VIL	-	Vss	-	0.2Vdd	V
Output "High" voltage	Vон	Іон=0.205mA	2.2	-	-	V
Output "Low" voltage	Vol	lo∟=1.2mA	-	-	0.8	V
Power supply current	IEE	Vee=-15V	-	2.0	-	mA
Power supply current	ldd	Vdd=5.0V	-	12.0	-	mA

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5.0 RECOMMENDED LC DRIVE VOLTAGE (VDD-VO)

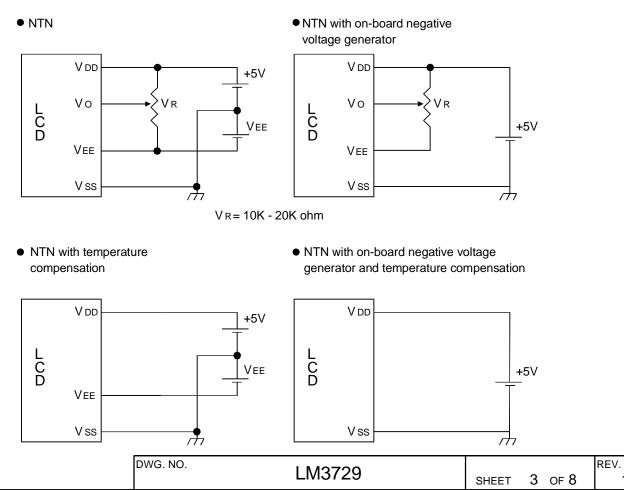
		,	,	Vdd=5.0±0.25V
Temperature	FSTN	FSTN-H	NTN	NTN-H
Ta= -20°C	-	19.6	-	24.3
Ta= 0°C	18.3	18.3	21.0	21.0
Ta= 25°C	17.3	17.3	18.5	18.5
Ta= 50°C	16.0	16.0	16.8	16.8
Ta= 70°C	-	15.0	-	15.7

6.0 BACKLIGHT SPECIFICATIONS:

Ta=20°C,60%RH,Darkroom.

Item	Symbol	Тур.	Max.	Unit
EL lamp input voltage	VEL	300	350	Vrms
EL input current	ICCFL	5.0	6.0	mA
Life to half initial brightness	-	10000	15000	Hours
EL lamp input frequency	FCCFL	25	60	KHz
Recommended backlight inverter	-	INV-12	-	-

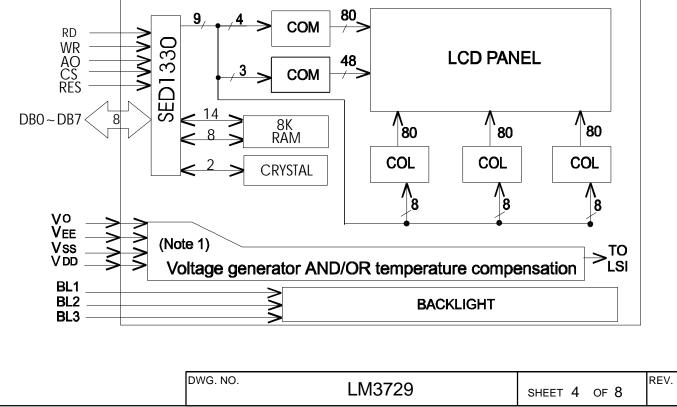
7.0 POWER SUPPLY



8.0 INTERFACE DESCRIPTION

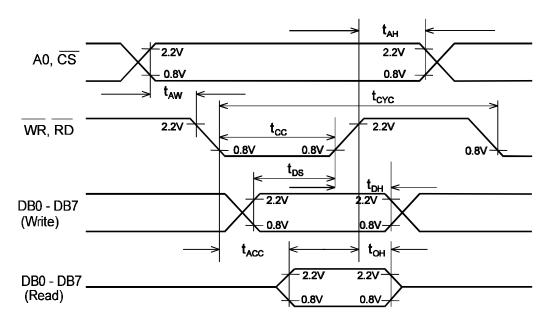
Pin No.	Symbol	I/O	Function
1	Vss	-	Ground (0V)
2	Vdd	-	Logic Supply Voltage (+5V)
3	Vo	-	LC drive voltage for contrast adjustment
4	RD		Read strobe signal (Active low)
5	WR		Write strobe signal (Active low)
6	AO		Data type selection
7	DB0	I/O	Bi-directional data bus line 0
8	DB1	I/O	Bi-directional data bus line 1
9	DB2	I/O	Bi-directional data bus line 2
10	DB3	I/O	Bi-directional data bus line 3
11	DB4	I/O	Bi-directional data bus line 4
12	DB5	I/O	Bi-directional data bus line 5
13	DB6	I/O	Bi-directional data bus line 6
14	DB7	I/O	Bi-directional data bus line 7
15	CS		Chip Select (Active low)
16	RES		Reset (Active low)
17	Vee	I(O)	Negative voltage input for LC drive (Negative voltage output for
			models with on-board negative voltage generator)
18	FG	-	Frame ground
19	N/C	-	No connection
20	N/C	-	No connection
BL1	VEL	-	EL backlight input voltage (from output of DC-AC inverter)
BL2	Vel	-	EL backlight input voltage (from output of DC-AC inverter)

9.0 BLOCK DIAGRAM:



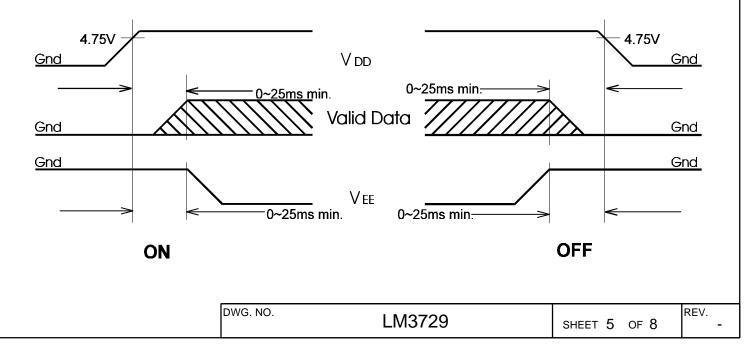
10.0 TIMING CHARACTERISTICS

Item	Symbol	Min.	Тур.	Max.	Unit
System cycle time	tcyc	575	-	-	nS
Control pulse width	tcc	220	-	-	nS
Address setup time	taw	30	-	-	nS
Address hold time	tан	10	-	-	nS
Data setup time	tDS	120	-	-	nS
Data hold time	tdн	10	-	-	nS
RD access time	tacc	-	-	120	nS
Output disable time	toн	10	-	50	nS



11.0 VOLTAGE SEQUENCING

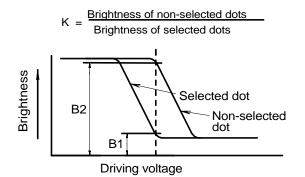
Always observe the following power supply ON/OFF sequence. Failure to so may cause latch up of CMOS LSI circuits or DC induced damage to LC panel.



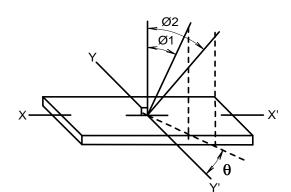
12.0 OPTICAL CHARACTERISTICS

Item	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Contrast ratio	K	Ø=20° θ=0°	4	-	-	-
Viewing angle	Ø2-Ø1	θ=0° K <u>></u> 1.4	40	-	-	Deg.
	θ	Ø=20° K=1.4	±30	-	-	Deg.
Response time Rise	tr	Ø=20° θ=0°	-	150	250	mS
Fall	tf	Ø=20° θ=0°	-	150	250	mS

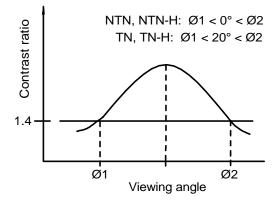




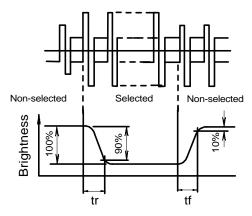
DEFINITION OF ANGLES Ø AND θ



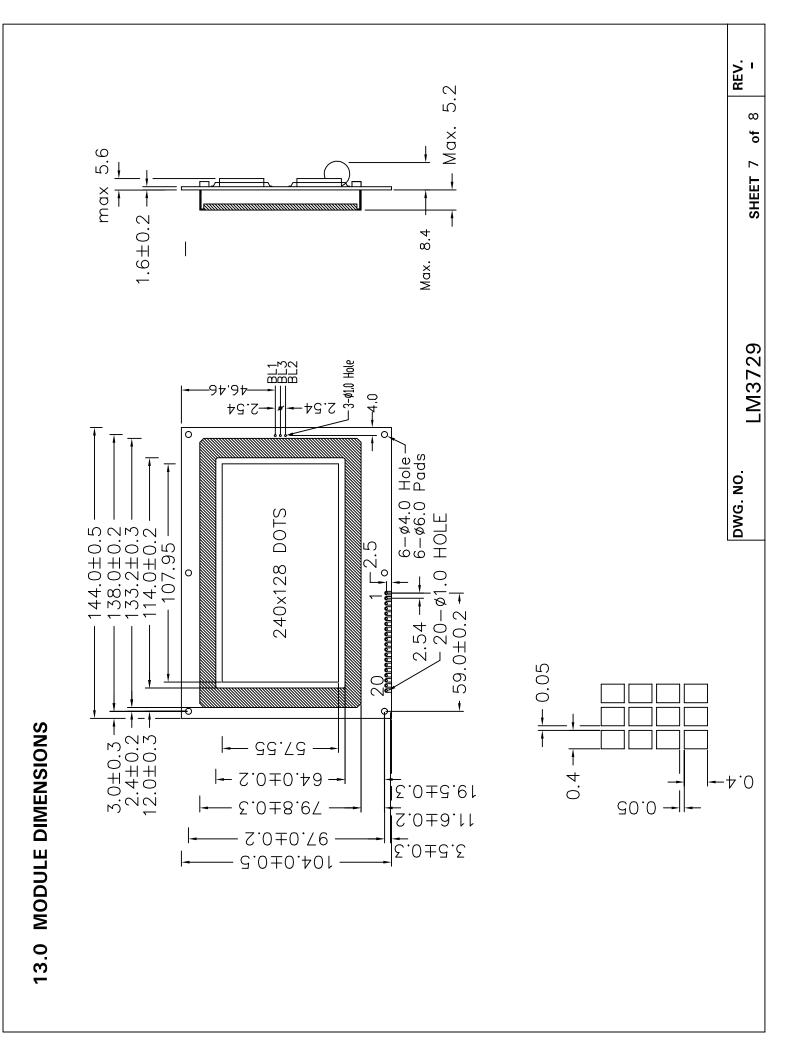
CONTRAST VERSUS VIEWING ANGLE

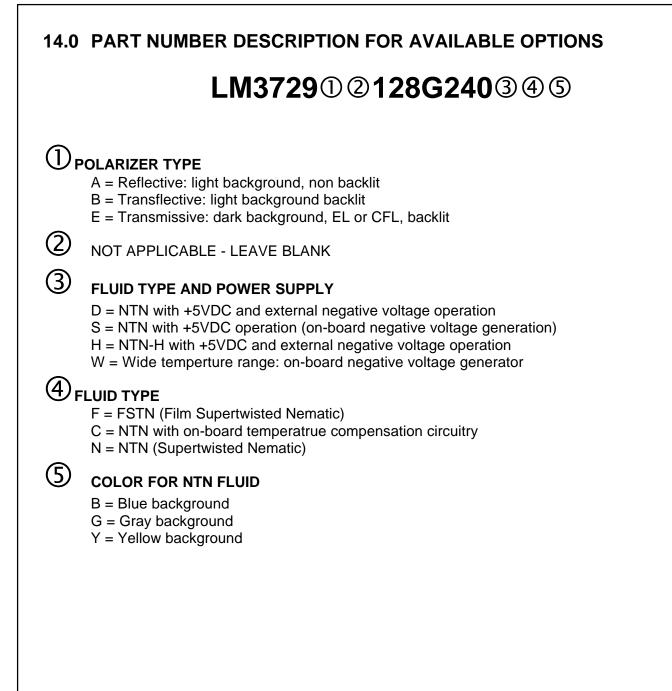


DEFINITION OF OPTICAL RESPONSE



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