

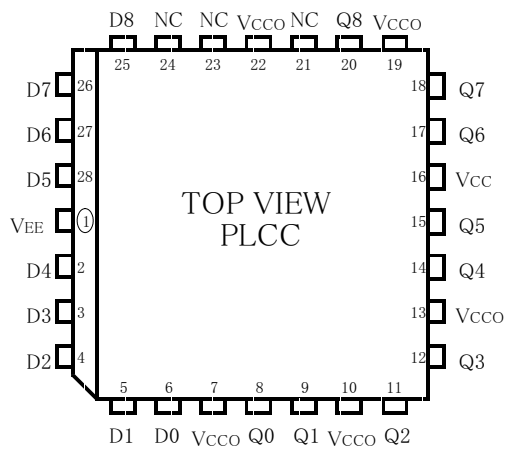
Description

The NB10EP122 is a 9-bit buffer. The device contains nine non-inverting buffer gates.

Features

- 500ps max. propagation delay
- 75k Ω internal pulldown resistors

Pin assignment

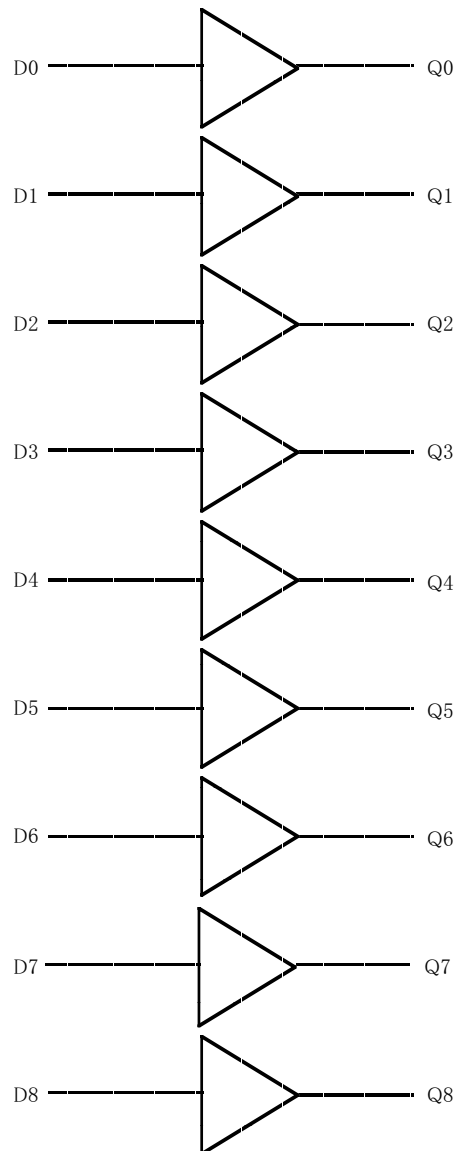


*All VCC and VCC0 pins are tied together on the die.

Pin names

Pin	Function
D0-D8	Data inputs
Q0-Q8	Data outputs

Block diagram



NB10EP122

DC Electrical Characteristics

$T_a=0\sim 85^{\circ}\text{C}$

$V_{CC}=0\text{V}; V_{EE}=-5.2\text{V} \pm 5\%$

Symbol	Characteristic	Min	Typ	Max	Unit
I _{IH}	Input High Current			200	μA
I _{EE}	Power Supply Current		28	39	mA

AC Electrical Characteristics

$T_a=0\sim 85^{\circ}\text{C}$

$V_{CC}=0\text{V}; V_{EE}=-5.2\text{V} \pm 5\%$

Symbol	Characteristic	Min	Typ	Max	Unit
t _{PLH} t _{PHL}	Propagation Delay to Output D to Q	150	350	500	ps
t _{skew}	Within-Device Skew D to Q		75		ps
t _r t _f	Rise/Fall Time 20-80%	300	425	800	ps

1. Within-device skew is defined as identical transitions on similar paths through a device.