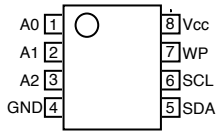
# BR24C08/F/FJ/FV
# BR24C16-W/F-W/FJ-W/FV-W <Under development>

## Features

- 8k bit serial EEPROM organized as 1k × 8bit (BR24C08)
  16k bit serial EEPROM organized as 2k × 8bit (BR24C16)
- 2 wire bus serial interface
- Wide operating voltage range (3V operating)
  Read : 2.7~5.5V
  Write : 2.7~5.5V
- Low current consumption
  Active: 3mA MAX (Vcc=3~5V)
  Standby : 3 μA MAX (Vcc=3~5V)
- Clock frequency : 100kHz MAX(3V), 400kHz MAX(5V)
- Write cycle time : 10ms MAX (Vcc=3V, 5V)
- Address auto-increment function during read operation
- Automatic erase-before-write function during write operation
- Page write function : 16 byte
- Inadvertent write protection function
  Inadvertent write protection at low voltage (Vcc Lock-out function)
  WP(Write Protect) tunction
- Schmitt trigger circuit and noise filter are built into SCL and SDA pins
- 100,000 write cycle typical
- 10 years data retention
- Operating temperature range : -40~85˚C

## Pin Configurations

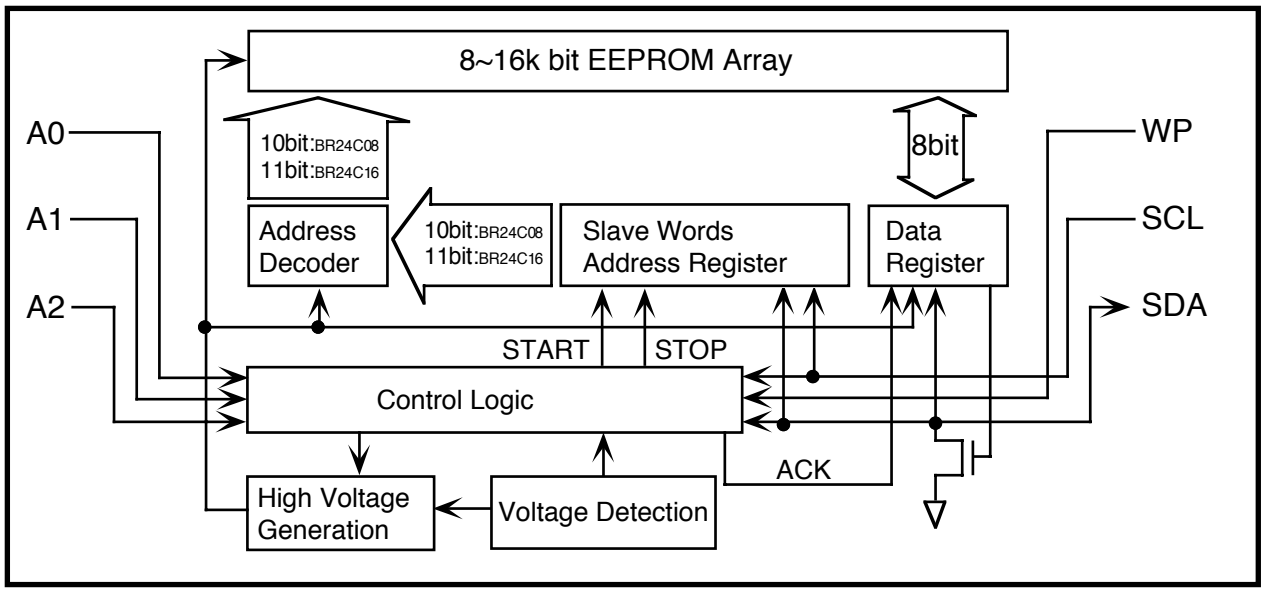



DIP8/SOP8/SOP-J8/SSOP-B8

## Pin Functions

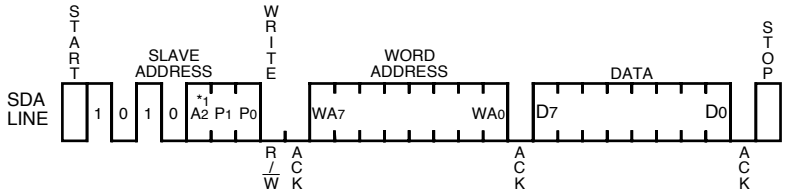
| Pin Names | Functions |
|---|---|
| A0, A1, A2 | Slave Address Inputs |
| GND | Ground |
| SDA | Serial Data Input/Output |
| SCL | Serial Data Clock |
| WP | Write Protect |
| Vcc | Power Supply |

## Block Diagram

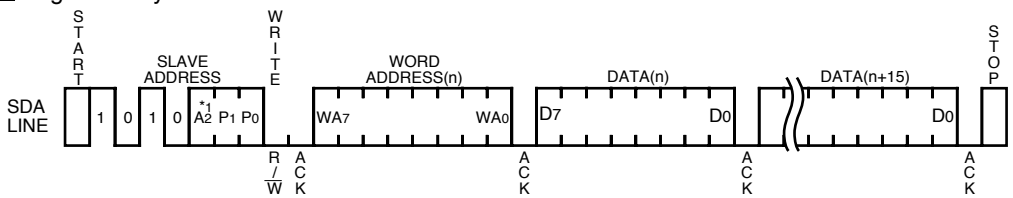


## Timing chart

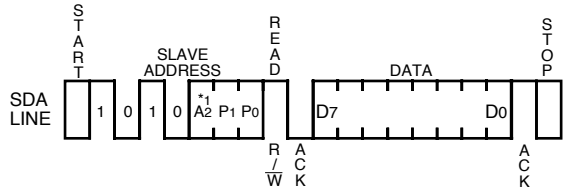
■ Byte write cycle



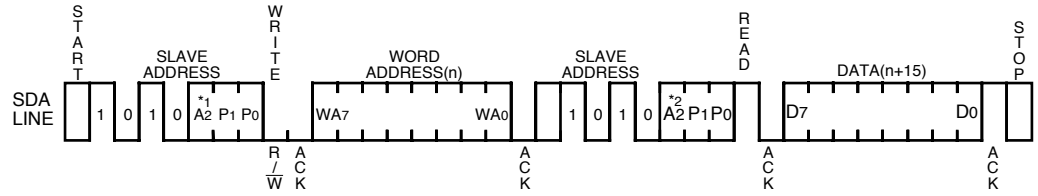

■ Page write cycle



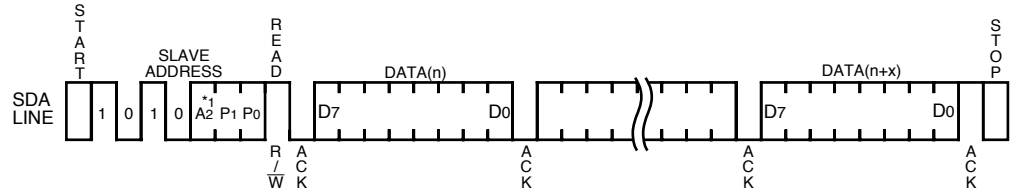

■ Current read cycle




■ Random read cycle




■ Sequential read cycle




*1: P2 (BR24C16)

Note : Double-cell type (BR24C08/F/FJ/FV).
Single-cell types (BR24C16/F/FJ/FV).
"-W" means double-cell type.