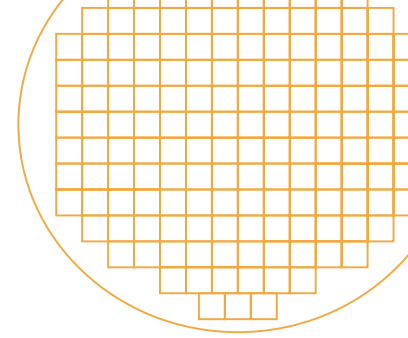


products PRISM[®] 2 AP



The PRISM[®] 2 AP chip set features the ISL3856 Wireless LAN Access Point Controller. The ISL3856 contains an ARM940 core controller, which implements both the IEEE 802.11b MAC protocol and Ethernet bridging functionality.

This chip set enables OEMs to build low cost, high performance Access Points.

- ISL3856 - Wireless LAN AP Controller
- HFA3863 - Baseband Processor with Rake Receiver and Equalizer
- HFA3783 - I/Q Mod/DeMod and Synthesizer
- HFA3683A - 2.4GHz RF/IF Converter and Synthesizer
- HFA3983 - 2.4GHz Power Amplifier with Detector

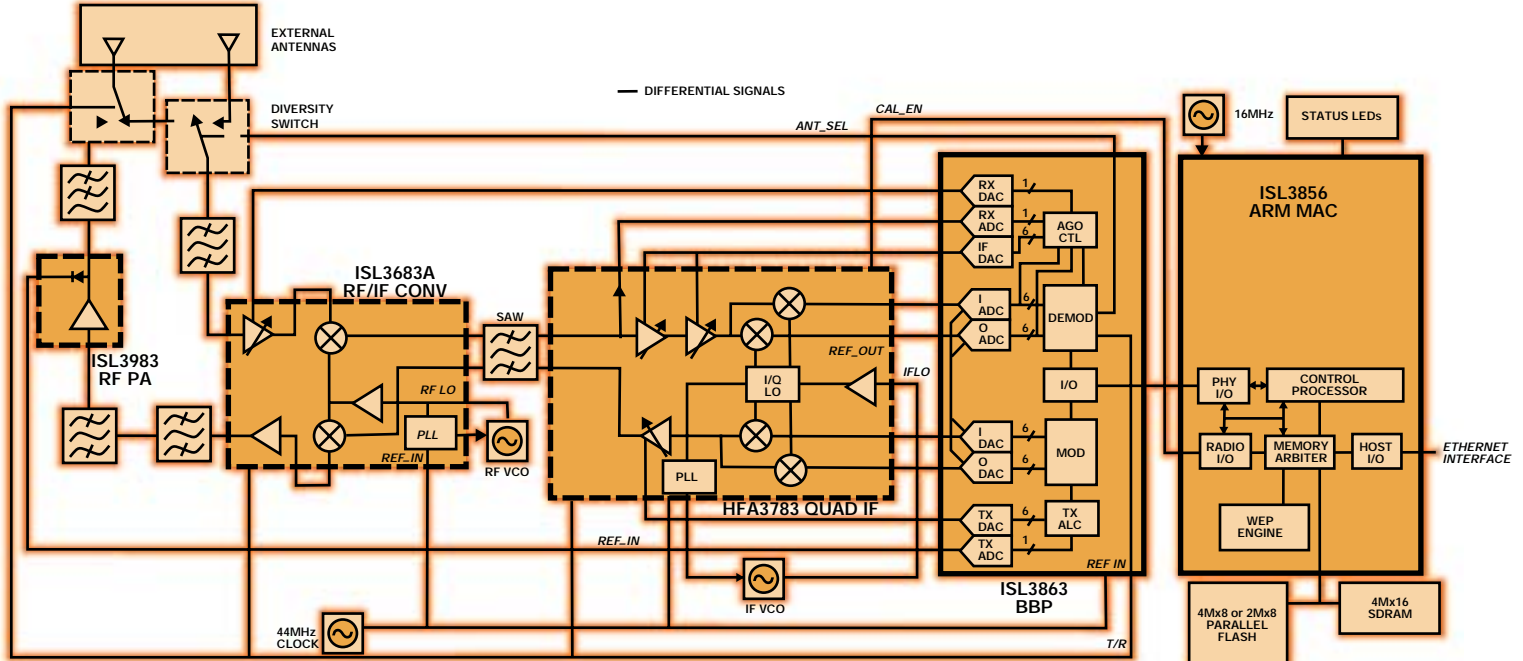
SPECIFICATIONS

Variable Data Rates	11, 5.5, 2 and 1Mbps
Frequency Band	2.4GHz ISM Band
Sensitivity	(BER 8xE-5) at -84 dBm (11Mbps)
TX Power Output	14 min, 15 typ, 16 dBm max
Dual Mode	IEEE 802.11b - 2, 5.5 and 11Mbps
Range	125ft Indoors*

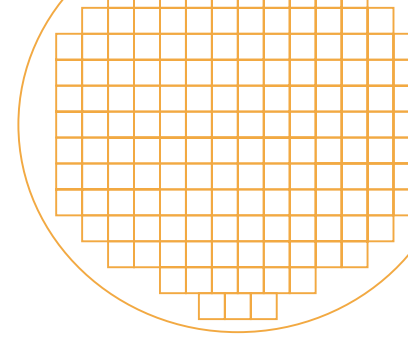
*Typical, 125ft @ 11 Mbps

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PRISM 2 AP Block Diagram



products PRISM[®] 2 AP



The PRISM 2 AP chip set includes reference designs, evaluation kits, firmware and software in addition to the highly integrated silicon. The PRISM 2 AP reference design is Wi-Fi™ certified by the Wireless Ethernet Compatibility Alliance (WECA), thus ensuring multi-vendor interoperability.

The unique design of the ISL36356A represents the most highly integrated access point (AP) reference design available for wireless applications in the home, small office and enterprise.

This design is available as part of an “AP Builder’s Kit”. This kit includes all of the components needed to productize the AP. Utilizing this turnkey approach can significantly reduce development costs, resource utilization, and time to market.

The AP Builder’s Kit includes:

- ISL36356A hardware reference design
- Firmware
- Kickstart software application (enables simplified user interface with AP)
- Web Development Kit (WDK) - Allows customization of HTML pages
- Sample end-user manual
- Manufacturing test tools