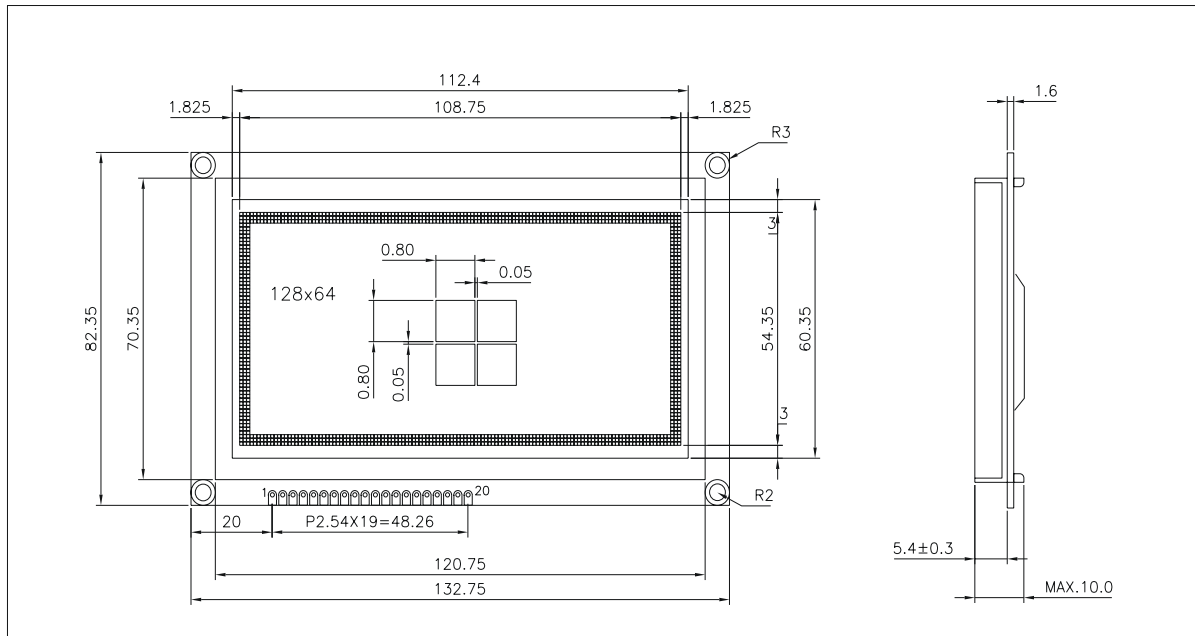


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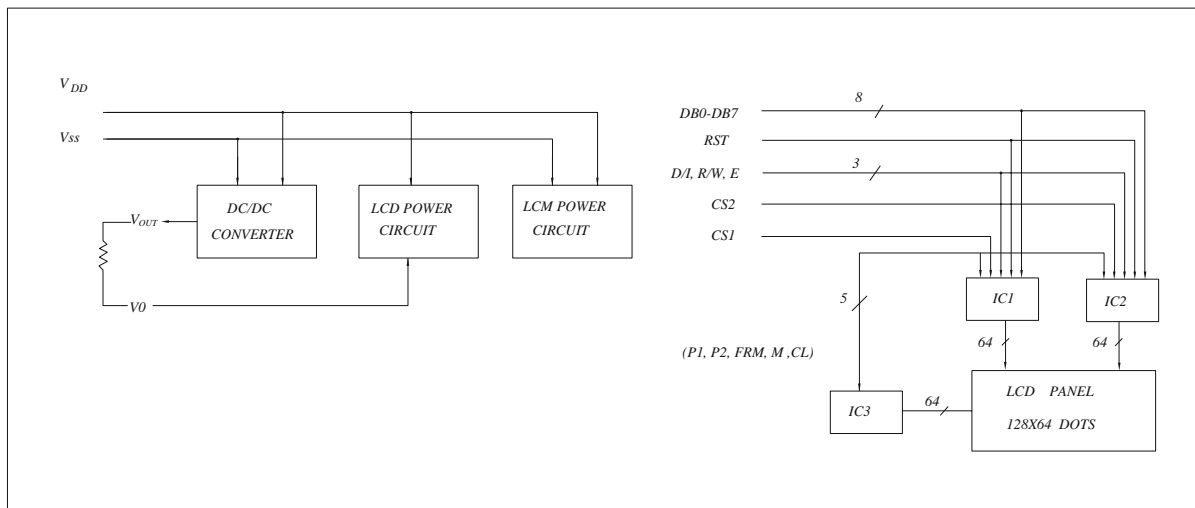
1/64 DUTY, 1/9 BIAS

(128 x 64 dots)

1.0 DIMENSIONAL DRAWING



2.0 BLOCK DIAGRAM



3.0 MECHANICAL SPECIFICATIONS

Item	Nominal Dimensions	Unit
Module Size(WxHxT)	132.75x82.35x10.0Max.	mm
Viewing Area(WxH)	112.4x70.35	mm
Dots x Dots(WxH)	128x64	dots
Dot Pitch(WxH)	0.85x0.85	mm
Dot Size(WxH)	0.80x0.80	mm

(128 x 64 dots)

4.0 ABSOLUTE MAXIMUM RATINGS

Item	Symbol	Test Condition	Standard Value		Unit
			Min.	Max.	
Supply Voltage for Logic	VDD-VSS	Ta=25°C	-0.3	7.0	V
Supply Voltage for LCD	VDD-Vo		0	19	V
Input Voltage	VI		-0.3	VDD+0.3	V
Operating Temperature	Topr	—	0	50	°C
Storage Temperature	Tstg	—	-10	60	°C

5.0 ELECTRICAL CHARACTERISTICS(Ta=25°C,VDD=5.0±0.25V)

Item		Symbol	Test Condition	Standard Value			Unit
				Min.	Typ.	Max.	
Supply Voltage for	Logic	VDD-VSS	—	4.5	5.0	5.5	V
	LCD	VDD-Vo	—	—	—	13.5	V
Supply Current for	Logic	IDD	—	—	10.0	15.0	mA
	LCD	—	—	—	—	—	mA
Operating Voltage for LCD (Recommended)		VDD-Vo	—	—	—	—	V
			25°C	—	12.5	—	V
			—	—	—	—	V
Input Voltage	'H'Level	VIH	High Level	0.7VDD	—	VDD	V
	'L'Level	VIL	Low Level	0	—	0.3VDD	V

6.0 INTERFACE PIN CONNECTIONS

Pin No	Symbol	Level	Description
1	VSS	—	Ground
2	VDD	—	Power supply for logic
3	Vo	—	Contrast Adjust
4	D/I	H/L	H:Data, L:Instruction code
5	R/W	H/L	Read/write selection H:Read L:Write
6	E	H,H/L	Enable signal for chip
7~14	DB0~DB7	H/L	Data line
15	CS1	H	Chip select signal for IC1
16	CS2	H	Chip select signal for IC2
17	RST	L	Reset at rising edge
18	Vout	—	Negative Voltage put
19	NC	—	
20	NC	—	