

VCC1 @ 125 MHz, Low Jitter SMD Oscillator for GigaBit Ethernet Applications

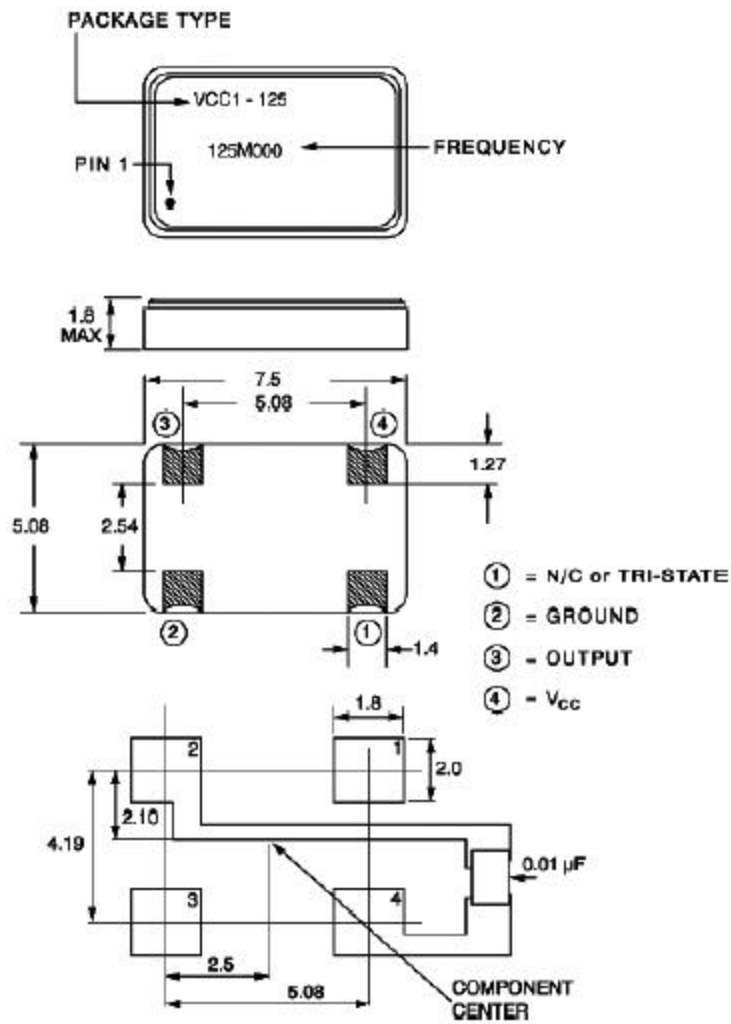
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Featuring

- Low Jitter 30T Design
- Designed for GigaBit Ethernet Applications
- 5.0 Vdc & 3.3 Vdc Option
- Tristate Option (High Impedance)
- TTL/CMOS Compatible
- [Product Data Sheet](#)



Frequency	125 MHz
Package Option	7.5 x 5 x 1.8 mm tall 4 pads
Voltage Options/ Load Drive	A = +5.0 Vdc ±10% 15pF B = +3.3 Vdc ±10% 15pF
Electrical Options	0 = No Tristate 60/40 Symmetry 1 = Tristate 60/40 Symmetry 2 = No Tristate 55/45 Symmetry 3 = Tristate 55/45 Symmetry
Tristate	N/C = OUTPUT Logic 1 = OUTPUT Logic 0 = High Impedance
Stability Options	A = ±100 PPM 0°C to +70°C B = ±50 PPM 0°C to +70°C C = ±100 PPM -40°C to +85°C D = ±50 PPM -40°C to +85°C
Total Jitter	5.0 V (≤ 16 ps PK to PK) 3.3 V (≤ 18 ps PK to PK) (as measured in a test circuit per Figure 4)
Start-Up	≤ 4.0 ms
Rise/ Fall Time	2 nsec Typical
Aging	< 10 PPM for 10 years at +40°C dynamic
Standard Load	HCMOS/TTL
Current	50 mA Maximum
Standard Packaging	Tape & Reel (1000 pc minimum reel)
Typical P/N	VCC1-B3A-125M000 C1 = 7.5x5x1.8mm tall 4 pads B = +3.3 Vdc ± 10 % 15 pF 3 = Tristate 45/ 55 Symmetry A = 100 ppm 0 to 70°C



Dimensions in mm.

