THS6101 UPSTREAM CABLE DRIVER FOR DOCSIS-COMPLIANT CABLE COMMUNICATIONS

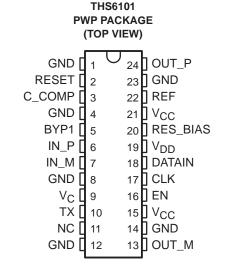
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- Low Noise and Distortion
 - Exceeds all DOCSIS Requirements
 - $- 64 \text{ dB HD3 at } 29.3 \text{ MHz}, V_{\Omega} = 56 \text{ dBmV}$
 - 83 dBmV IP3 at 65MHz
 - 47 dBmV Noise Level at Minimum Gain
- Wide Range of Output Power Levels
 - 62 dBmV RMS Output Power Drive
 - 54 dB Range of Programmable Gain
 - 6 dB Gain Steps
- Low Power Solution
 - 15 mW Disable-Mode Power Consumption
 - Power Consumption Scales With Gain
- Single-Supply 5 V Operation

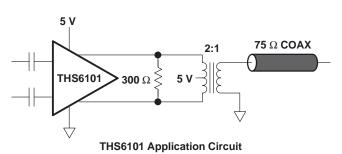
description

The THS6101 is a Class-A amplifier designed specifically for driving the upstream data path in Data Over Cable System Interface Specification (DOCSIS)-compliant cable modem applications. The DOCSIS places demanding constraints on the upstream drive amplifier, and the THS6101 is designed for full compliance with this industry standard.

Using only a single 5-V power supply, the THS6101 meets all of the DOCSIS distortion requirements. Differential signal paths throughout the device maintain signal integrity. An external 2:1 balun transformer converts the differential output signal to a single-ended output signal.



NC - No internal connection



The THS6101 architecture results in a very low third-harmonic distortion of –64 dBc at 29.3 MHz and an output power level of 56 dBmV. Signal linearity is maintained at maximum output levels, providing a third-harmonic distortion of –58 dBc at 29.3 MHz and an output power level of 62 dBmV. The device also exhibits an IP3 of 83 dBmV at a frequency of 65 MHz and an output power level of 60 dBmV, for two-tone intermodulation effects.

Output noise from the THS6101 is very low. At minimum gain, the device produces less than $10 \text{ nV}/\sqrt{\text{Hz}}$ of noise at its output. This translates to a level of -47 dBmV at the lowest symbol rate, which is lower than that required by DOCSIS. The THS6101 exhibits a disable-mode output noise of $2 \text{ nV}/\sqrt{\text{Hz}}$ or -61 dBmV.

The THS6101 output power level range exceeds the 58 dBmV required by DOCSIS. Its 62-dBmV RMS drive readily compensates for typical losses associated with the output transformer and tuner duplexer. The THS6101 provides a gain range of 54 dB, from 32 dB down to –22 dB in 6-dB steps. The gain is programmable through an SPI-compatible serial interface with a 4-bit control word.

The stringent power requirements of cable telephony and VOIP systems will greatly benefit from the low, industry-leading power consumption of the THS6101. With a typical 10% transmit duty-cycle, the 15-mW disable-mode power consumption of the THS6101 results in an average power consumption of 94 mW. Furthermore, the advanced architecture of the THS6101 reduces this average power consumption to 15 mW when transmitting at minimum gain.



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UPSTREAM CABLE DRIVER FOR DOCSIS-COMPLIANT CABLE COMMUNICATIONS

AVAILABLE OPTIONS

PACKAGED DEVICES		
TA	TSSOP [†] (PWP) PowerPAD™	
0°C to 70°C	THS6101CPWP	
–40°C to 85°C	THS6101IPWP	

[†]This package is available taped and reeled. To order this packaging option, add an R suffix to the part number (e.g., THS6101CPWPR).

Absolute maximum ratings over operating free-air temperature range (unless otherwise noted) ‡

Supply voltage, V _{CC}	13.2 V
Supply voltage, V _{DD}	5.5 V
Input voltage, V _I	V _{CC}
Output current, I _O	
Differential input voltage, V _{ID}	±5 V
Continuous total power dissipation	See Dissipation Rating Table
Operating free air temperature, T _A : C suffix	0°C to 70°C
I suffix	–40°C to 85°C
Maximum junction temperature	150°C
Storage temperature, T _{stq}	
Lead temperature 1,6 mm (1/16 inch) from case for 10 seconds	300°C

[‡] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

DISSIPATION RATING TABLE

PACKAGE	AGE (°C/W) θJC (°C/W)		T _A = 25°C POWER RATING
PWP(24)	PWP(24) 30.1 0.		3.32 W

[§] This data was taken using 2 oz. Trace and copper pad that is soldered directly to a 3 in. × 3 in. PCB.

recommended operating conditions

		MIN	TYP MAX	UNIT
Operating free-air temperature, T _A	C suffix	0	70	°C
Operating free-all temperature, 1A	I suffix	-40	85	C

power supply

PRODUCT PREVIEW

	PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Vcc	Analog supply voltage		4.5	5	5.5	V
V_{DD}	Logic supply voltage		4.5	5	5.5	V
		Transmit mode, G = 32 dB (Gain code = 9)		186		
		Transmit mode, G = 26 dB (Gain code = 8)		125		
	Quiescent current	Transmit mode, G = 20 dB (Gain code = 7)		68		m A
ICC	Quiescent current	Transmit mode, G = 14 dB (Gain code = 6)		40		mA
		Transmit mode, G = 8 dB (Gain code = 5)		30		
		Transmit mode, G = 2 dB (Gain code = 4)		22		



[¶] The data shown for θ_{JA} was taken using the recommended PowerPAD™ PCB guidelines.

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recommended operating conditions (continued)

power supply

PARAMETER		TEST CONDITIONS	MIN	TYP	MAX	UNIT
		Transmit mode, G = -4 dB (Gain code = 3)		20		
		Transmit mode, G = -10 dB (Gain code = 1)		16		
ICC Quiescent current	Transmit mode, G = -16 dB (Gain code = 1)		16		mA	
	Transmit mode, G = -22 dB (Gain code = 0)		15			
		Disable mode (TX = 0)		3		

electrical characteristics, V_{CC} = V_{DD} = 5 V, V_{IN} = 0.25 V_{p-p}, R_L = 75 Ω , T_A = 25°C (unless otherwise noted)

input characteristics

	PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Z _{IN}	Input impedance	Single-ended		750		W
VIN	Input voltage range	Differential		62.5	250	mV
CMRIN	Common mode rejection	V _{IN,CM} = 1 V, f _{IN} = 10 MHz, Gain = 2		60		dB
	Gain range		-22		32	dB
	Absolute gain accuracy		-1		1	dB
	Gain step width		5.8	6	6.2	dB

output characteristics

PARAMETER	TEST CONDITIONS	MIN TY	P MAX	UNIT
Output voltage range	V _{CC} = 5 V	6.	32	V _{p-p}
Output impedance	Transmit mode		75	W
Output impedance	Disable mode		75	W
	Gain = 32 dB, TX = 1	94	1.5	
Output noise spectral density	Gain = -22 dB, TX = 1		10	nV/√ Hz
	All gain codes, TX = 0		2]

dynamic performance

PARAMETER	TEST CON	NDITIONS	MIN TYP	MAX	UNITS
Full power bandwidth	Gain code = 9, Gain code < 9,	V_{in} = 35 dBmV sine wave, V_{in} = 39 dBmV sine wave	100		MHz
2 nd Harmonic distortion	f: _ 44 MHz	VOUT = 56 dBmV	-62		dBc
2114 Harmonic distortion	f _{in} = 44 MHz	V _{OUT} = 62 dBmV	-57		ubc
3 rd Harmonic distortion	f _{in} = 29.3 MHz	V _{OUT} = 56 dBmV	-64		dBc
3.4 Harmonic distortion	1 _{IN} = 29.3 WI 12	V _{OUT} = 62 dBmV	-58		UDC
3 rd Order intercept	f _{in} = 29.3 MHz	V _{OUT} = 60 dBmV	83		dBmV
Two-tone 3 rd order distortion	f_{in1} = 42 MHz, f_{in2} = 42.2 MHz, Gain code = 9 (G = 32 dB)	V _{OUT} = 60dBmV, V _{IN} = 30dBmV,	-65		dBc
rwo-tone 3'4 order distortion	$ f_{\text{in1}} = 65 \text{ MHz}, $ $ V_{\text{OUT}} = 60 \text{ dBmV}, $ $ Gain \ \text{code} = 9 \ (G = 32 \text{ dB}) $	f _{in2} = 65.2 MHz, V _{IN} = 30 dBmV,	-62.5		dBc
Spurious-free dynamic range	f _{in} = 65 MHz, Gain code = 9 (G = 32 dB)	V _{OUT} = 60 dBmV,	TBD		dBc
Signal feedthrough and spurious emissions	f _{in} = 65 MHz,	Disable mode	TBD		dBmV



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electrical characteristics, V_{CC} = V_{DD} = 5 V, V_{IN} = 0.25 V_{p-p}, R_L = 75 Ω , T_A = 25 °C (unless otherwise noted) (continued)

power control

PARAMETER	TEST CONDITIONS	MIN TYP MAX	UNIT
Turn on/turn off time		TBD	ms
Enable/disable output transient voltage	Gain code = 9	100	mV
Enable/disable output transient voltage	Gain code ≤ 5	7	IIIV
Transient slew rate	All gain codes	TBD	V/μs
Gain code 0	G = -22 dB (min gain)	10000000	
Gain code 1	G = -16 dB	10000001	
Gain code 2	G = -10 dB	10000010	
Gain code 3	G = -4 dB	10000011	
Gain code 4	G = 2 dB	10000100	
Gain code 5	G = 8 dB	10000101	
Gain code 6	G = 14 dB	10000110	
Gain code 7	G = 20 dB	10000111	
Gain code 8	G = 26 dB	10001000	
Gain code 9	G = 32 dB (max gain)	10001001	

device

	PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	Units		
LOGIC INP	LOGIC INPUT LEVELS							
VIH	High-level input voltage	Relative to GND		2		V		
V _{IL}	Low-level input voltage	Relative to GND			0.8	V		
lін	High-level input current	V _{IH} = 5 V		20	100	nA		
I _{IL}	Low-level input current	V _{IL} = 0.5 V		0.9	2	mA		
SERIAL IN	TERFACE TIMING							
t _c (CLK)	Clock cycle time		20			ns		
t _{su(DS)}	DATAIN to CLK setup time		5			ns		
th(DH)	DATAIN to CLK hold time		5			ns		
t _{su} (ENS)	EN to CLK setup time		10			ns		
^t h(ENH)	EN to CLK hold time		10			ns		

PARAMETER MEASUREMENT INFORMATION

The THS6101 is a multistage amplifier specifically designed to drive the return path in DOCSIS-compliant cable modems. The device is designed to comply with the DOCSIS requirements, providing sufficient output power, low output noise and distortion, and a clean transition to the low-power mode.

The THS6101 has a current-mode open collector output stage. A 2:1 balun transformer provides power from the 5-V connection at its primary center tap and converts the THS6101 differential output to a single-ended output for driving a coaxial cable. An external $300-\Omega$ resistor across the device outputs provides proper termination of the line.



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PRINCIPLES OF OPERATION

The gain of the THS6101 ranges from -22 dB at the minimum setting to 32 dB at the maximum setting. A serial SPI-compatible interface provides control over the gain setting. Figure 1 illustrates the required timing of the serial interface.

The following table matches gain code with its corresponding gain value.

CODE	GAIN
10000000	–22 dB
10000001	–16 dB
10000010	–10 dB
10000011	–4 dB
10000100	2 dB
10000101	8 dB
10000110	14 dB
10000111	20 dB
10001000	26 dB
10001001	32 dB

The last four bits of the code represents the THS6101 gain setting.

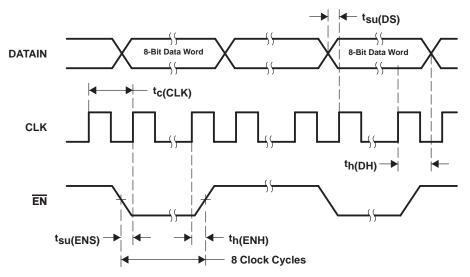


Figure 1. Serial Interface Timing

The THS6101 provides 9 coarse gain steps of 6 dB, for a full gain range of 54 dB. Variable attenuation incorporated into many DOCSIS-compliant cable modem solutions provides the fine gain control required by DOCSIS. To produce the correct output power levels, the THS6101 requires a nominal input power level of 25 dBmV to 30 dBmV. These input levels correctly interpolate between the 6-dB steps produced by the THS6101.

Since cable modem transmissions are burst-mode in nature, the THS6101 is equipped with both a transmit and a disable mode. The TX input pin controls this functionality. With the TX pin pulled high, the THS6101 is powered-up and ready to transmit. Pulling the TX pin low reduces the quiescent current of the THS6101 to 2 mA and shuts down the output stage of the amplifier, isolating it from the line.

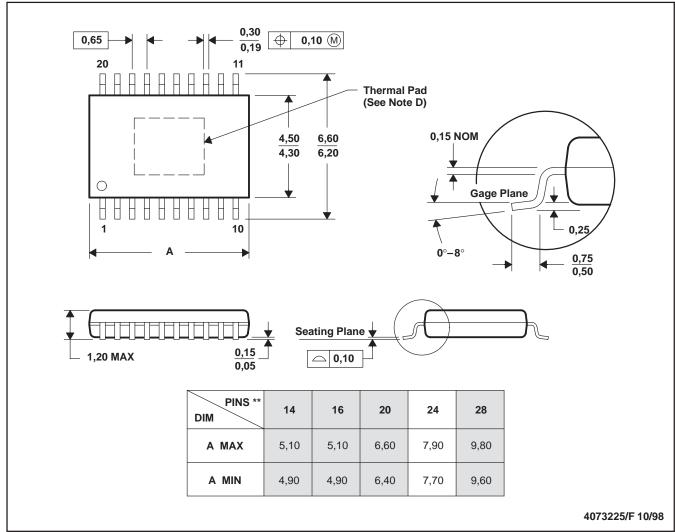


MECHANICAL DATA

PWP (R-PDSO-G**)

PowerPAD™ PLASTIC SMALL-OUTLINE

20 PINS SHOWN



- NOTES: A. All linear dimensions are in millimeters.
 - B. This drawing is subject to change without notice.
 - C. Body dimensions do not include mold flash or protrusions.
 - D. The package thermal performance may be enhanced by bonding the thermal pad to an external thermal plane. This pad is electrically and thermally connected to the backside of the die and possibly selected leads.
 - E. Falls within JEDEC MO-153

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PRODUCT PREVIEW

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