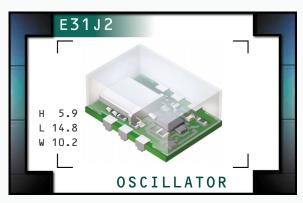
E31J2 Series

- PECL Output VCXO
- 5.0V supply voltage
- 6 pad PCB SMD package with J-leads
- Stability to 20ppm
- Output Enable/Disable available
- Complementary Output available
- Available on Tape and Reel





ELECTRICAL SPECIFICATIONS

| | <u>-</u> | | | | | |
|---|--|---|---|---|---|--|
| Frequency Range | | | 19.440MHz to 180.000MHz | | | |
| Operating Temperature Range | | | | 0°C to 70°C or -40°C to 8 | 55°C | |
| Storage Temperature Range | | | | -55°C to 125°C | | |
| Supply Voltage (V _{cc}) | | | 5.0V _{DC} ±5% | | | |
| Input Current | | | | 100mA Maximum | | |
| Logic Type | | | | 100KH | | |
| Frequency Tolerance / Stability | Inclusive of Operating Temp Range, Supply Voltage, | | | * | | |
| | Load, and A | Load, and Aging @25°C over 10 years | | ±20ppm Maximum | | |
| Output Voltage Logic High (V _{OH}) | | V _{cc} -1.025V _{DC} Minimum | | | | |
| Output Voltage Logic Low (V _{oL}) | | | V _{CC} -1.620V _{DC} Maximum | | | |
| Rise Time / Fall Time | • | | | 2 nSeconds Maximum | | |
| Duty Cycle | at 50% of waveform | | | 50 ±10(%) | | |
| | | | | 50 ±5(%) | | |
| Load Drive Capability | | | | 50 Ohms into V_{CC} -2.0 V_{DC} | | |
| Additional Output / Logic Control | | | No Connect and Single Output | | | |
| | | | | Enable/Disable and Single O |)utput | |
| | | | | No Connect and Complemen | tary Output or | |
| | | | | Enable/Disable and Comple | mentary Outpu | |
| Enable/Disable Input Voltage | V_{IL} of V_{CC} -1.475 V_{DC} Maximum | | | Enables Output | | |
| | No Connection | | | Enables Output | | |
| | $V_{\rm IH}$ of $V_{\rm CC}$ -1. | V_{IH} of V_{CC} -1.165 V_{DC} Minimum | | | Disables Output: Logic Low | |
| | | | | | Disables Complementary Output: Logic Hi | |
| Start Up Time | | | 10 mSeconds Maximum | | | |
| RMS Phase Jitter | 5 Phase Jitter $FJ = 12kHz \text{ to } 20MHz$ | | | 1 pSec Maximum | | |
| Absolute Pull Range (APR) | Inclusive of Operating Temp Range, Supply Voltage, | | | ±50ppm Minimum | | |
| | Load, and A | Aging @25°C over 10 ye | ears | | | |
| Linearity | | | | 20%, 15%, or 10% Maximum | | |
| Control Voltage (V _c): Test Conditions for APR | | | | 2.5V _{DC} ±2.0V _{DC} | | |
| Control Voltage Range (V _{CR}) | | | | 0.0V _{DC} to V _{CC} | | |
| Center Control Voltage | | | | 2.5V _{DC} | | |
| Transfer Function | | | | Positive Transfer Characteristic | | |
| Input Impedance | | | | 50k0hms Typical | | |
| Modulation Bandwidth at -3dB with Control Voltage of +2.5V _{DC} | | | 10kHz Minimum | | | |
| MANUFACTURER CATEGORY | SERIES | PACKAGE | VOLTAGE | CLASS | REV = DAT | |
| ECLIPTEK CORP. OSCILLATOR | E31J2 | 6-PCB-J | 5.0V | 0S74 | 11/01 | |

PART NUMBERING GUIDE E31J2 F 3 A 2 C - 155.520M TR FREQUENCY TOLERANCE & STABILITY/ **AVAILABLE OPTIONS OPERATING TEMPERATURE RANGE** Blank=Tubes D=±50ppm Maximum over 0°C to +70°C TR = Tape and Reel (Standard) E=±25ppm Maximum over 0°C to +70°C F=±20ppm Maximum over 0°C to +70°C **FREQUENCY** H=±50ppm Maximum over -40°C to +85°C **ADDITIONAL OUTPUT** APR A=No Connect and Single Output 3=±50ppm Minimum B=Enable/Disable and Single Output C=No Connect and Complementary Output LINEARITY D=Enable/Disable and Complementary Output A=20% **DUTY CYCLE** B=15% C=10% $1=50\pm10(\%), 2=50\pm5(\%)$ SUGGESTED SOLDER PAD LAYOUT MECHANICAL DIMENSIONS ALL DIMENSIONS IN MILLIMETERS ALL DIMENSIONS IN MILLIMETERS FRONT VIEW TOP VIEW SIDE VIEW 148 5.80 Solder MAX MAX Land 5 4 3.0 (x6) |-9.8 5.9 1.6 (x6) MAX MAX 2 3 1 0.94 ЦЦ 2 540 0.60 7.620 0.40 ±0.102 ±0.203 ±0.05 ±0.05 0.94 5.080 ±0.203 Pin 1: Voltage Control Pin 4: Output Pin 2: Enable/Disable or No Connect Pin 5: Complementary Output or No Connect Pin 3: Case Ground Pin 6: Supply Voltage Tolerances = ± 0.1 TAPE AND REEL DIMENSIONS ALL DIMENSIONS IN MILLIMETERS M− D ØH7 Ε (Access Hole at Slot Location) В U ØN С R Width S Depth (Tape Slot in Core for Tape Start) ØO **-** G -ØP TAPE REEL 1.5 MIN 50 MIN 20.2 MIN 13±.2 40 MTN $24 \pm .3$ 11.5 ±.1 10.75 ±.1 $4 \pm .2$ 2±.1 2.5 MIN 30.4 MAX 360 MAX 24.4+2-0 1000 B0* 1.5 +.1-0 A0* .4 ±.05 K0* 10 MIN *Compliant to EIA 481A ENVIRONMENTAL/MECHANICAL SPECIFICATIONS MARKING SPECIFICATIONS Line 1: ECLIPTEK Characteristic Seal Integrity Bubble test in Perfluorocarbon at +125°C ±5°C for 60 seconds Line 2: XX.XXX M minimum (internal crystal only). Frequency in MHz (5 Digits Maximum + Decimal) Solderability Sn63 Solder dip at +230°C ±5°C for 5 seconds/95% coverage. Marking Permanency 10 Strokes with brush after 1 minute soak in solvent, 3 times. Line 3: XX Y ZZ Shock Random drop on hard wooden plate 3 times from a height Week of Year of 20cm. Last Digit of Year Frequency with an amplitude of 1.5mm sweeping between 10Hz Ecliptek Manufacturing Identifier Vibration to 55Hz within 1 minute (approximately) for 2 hours minimum on each axis (X, Y and Z) for a total of 6 hours. MANUFACTURER CATEGORY SERIES PACKAGE VOLTAGE CLASS REV - DATE

E31J2

OSCILLATOR

ECLIPTEK CORP.

6-PCB-J

0574

5.0V

11/01