

## Technical Specification for Optical Transceiver Module

### SDM7301-XC

### SDM7301-GC

- |  |   |   |
|--|---|---|
| <input checked="" type="checkbox"/> 155.52Mb/s<br><input checked="" type="checkbox"/> Short Haul<br><input checked="" type="checkbox"/> Intermediate Reach<br><input checked="" type="checkbox"/> Single 5.0 V<br><input checked="" type="checkbox"/> 1.3 μm<br><input type="checkbox"/> Transmitter | <input type="checkbox"/> 622.08Mb/s<br><input type="checkbox"/> Long Haul<br><input type="checkbox"/> Long Reach<br><input type="checkbox"/> Single 3.3 V<br><input type="checkbox"/> 1.55 μm<br><input type="checkbox"/> Receiver<br>( <input type="checkbox"/> 2R / <input type="checkbox"/> 3R ) | <input type="checkbox"/> other _____<br><input type="checkbox"/> other _____<br><input type="checkbox"/> other _____<br><input type="checkbox"/> other _____<br><input checked="" type="checkbox"/> Transceiver<br>( <input type="checkbox"/> 2R / <input checked="" type="checkbox"/> 3R ) |
|--|---|---|



Sumitomo Electric reserves the right to make changes in this specification without prior notice.

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**#Safety Precaution Symbols** This specification uses various picture symbols to prevent possible injury to operator or other persons or damage to properties for appropriate use of the product. The symbols and definitions are as shown below. Be sure to be familiar with these symbols before reading this specification.

- |  |
|--|
| <b>Warning</b> Wrong operation without following this instruction may lead to human death or serious injury.   |
| <b>Caution</b> Wrong operation without following this instruction may lead to human injury or property damage. |

Example of picture symbols    indicates prohibition of actions. Action details are explained thereafter.  
    indicates compulsory actions or instructions. Action details are explained thereafter.

## 1. General

SDM7301-XC is a series of compact and high speed performance digital optical transceiver module ideally designed for versatile high speed network applications. 1300nm high speed InGaAsP FP-LD and InGaAs PIN-PD are provided as a light source and a detector, respectively. Transceiver module has PC board mountable package with electrical and optical interfaces.

|                        |                                   |
|------------------------|-----------------------------------|
| * Data Rate            | 155.52 Mbps, NRZ                  |
| * Duty Cycle           | 50%                               |
| * Power Supply Voltage | Single +5V                        |
| * Electrical Interface | PECL                              |
| * Fiber Coupled Power  | -8 ~ -15dBm (Typ. -11dBm) for SMF |
| * Sensitivity          | -8 ~ -28dBm (Typ. -37dBm)         |
| * Connector Interface  | SC Duplex Connector               |

The features of SDM7301-XC are listed below.

|                  |  |
|------------------|--|
| * Features       | Low Power Supply Voltage / Low Power Consumption<br>Plastic Molded Package<br>Multi-sourced Footprint  |
| Transmitter..... | Uncooled FP-LD with Automatic Power Control IC<br>Optical Output Shut-down ( Disable )<br>Laser Bias Current Monitor / Rear Facet Monitor<br>Class 1 Laser Product ( IEC 825-1 and FDA 21 CFR 1040.10 and 1040.11) |
| Receiver.....    | Wide Dynamic Range<br>Built-in Clock Recovery (including SAW filter)<br>ITU-T G.957 / G.958 and Bellcore TA-NWT-000253 Compliant<br>Signal Detect (FLAG) Function  |

## 2. Block Diagram

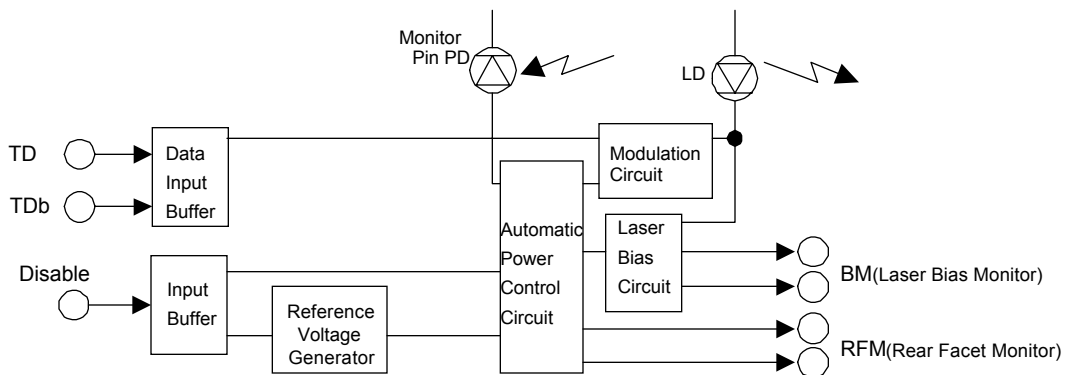


Figure 1-1. Block Diagram (Transmitter)

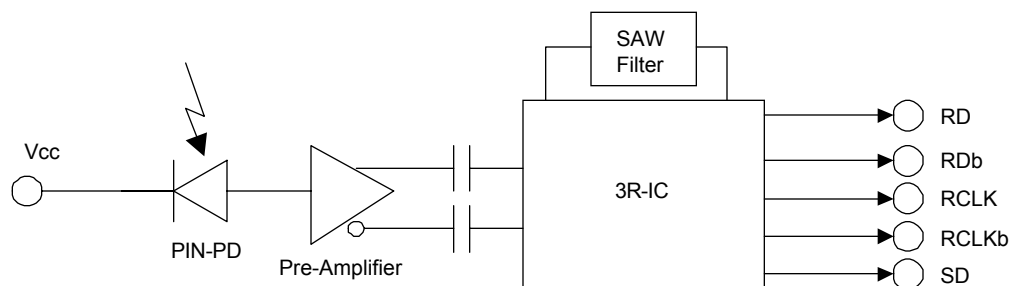


Figure 1-2 Block Diagram (Receiver)

### 3. Package Dimension

All dimensions are in mm.

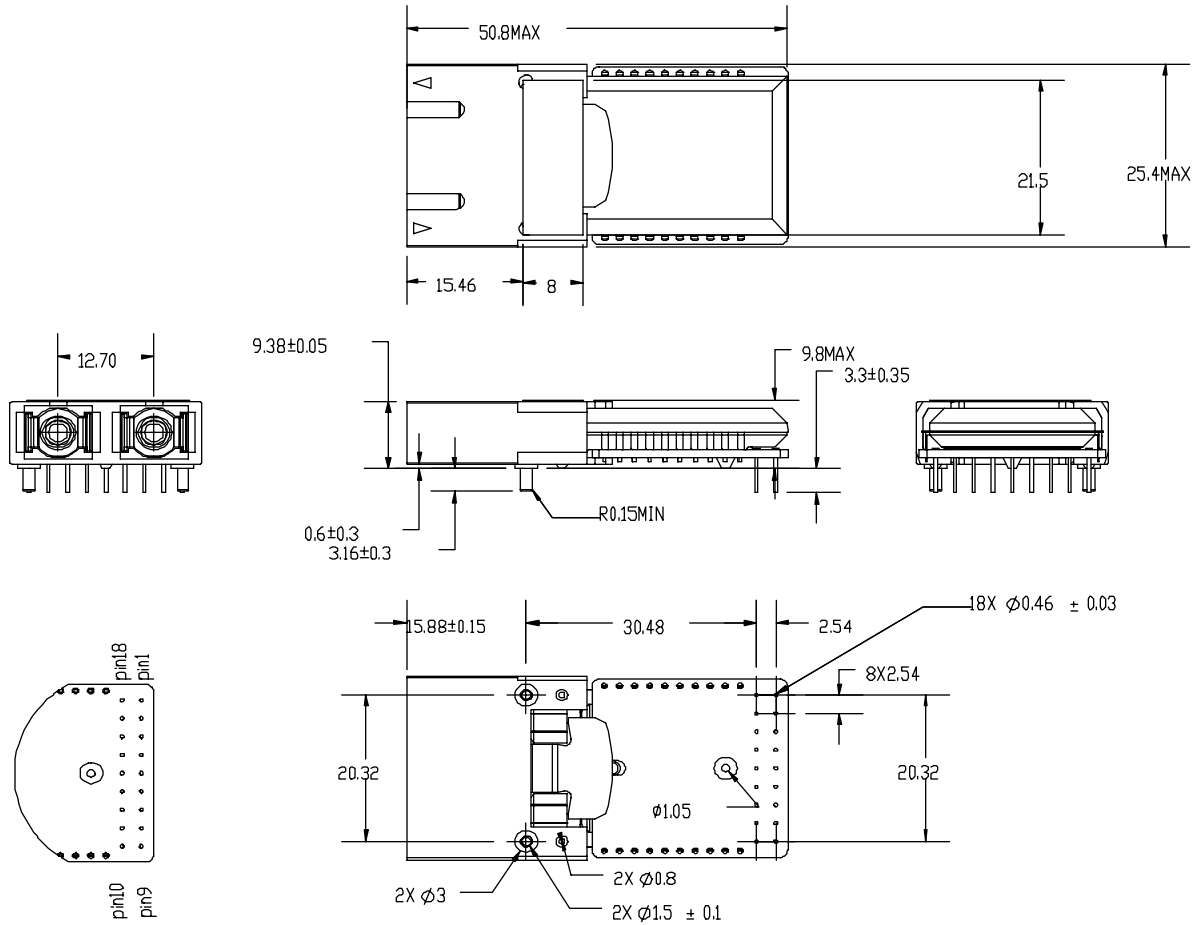


Figure2-1.OutlineDimensions(SCM7301-XC)

All dimensions are in mm.

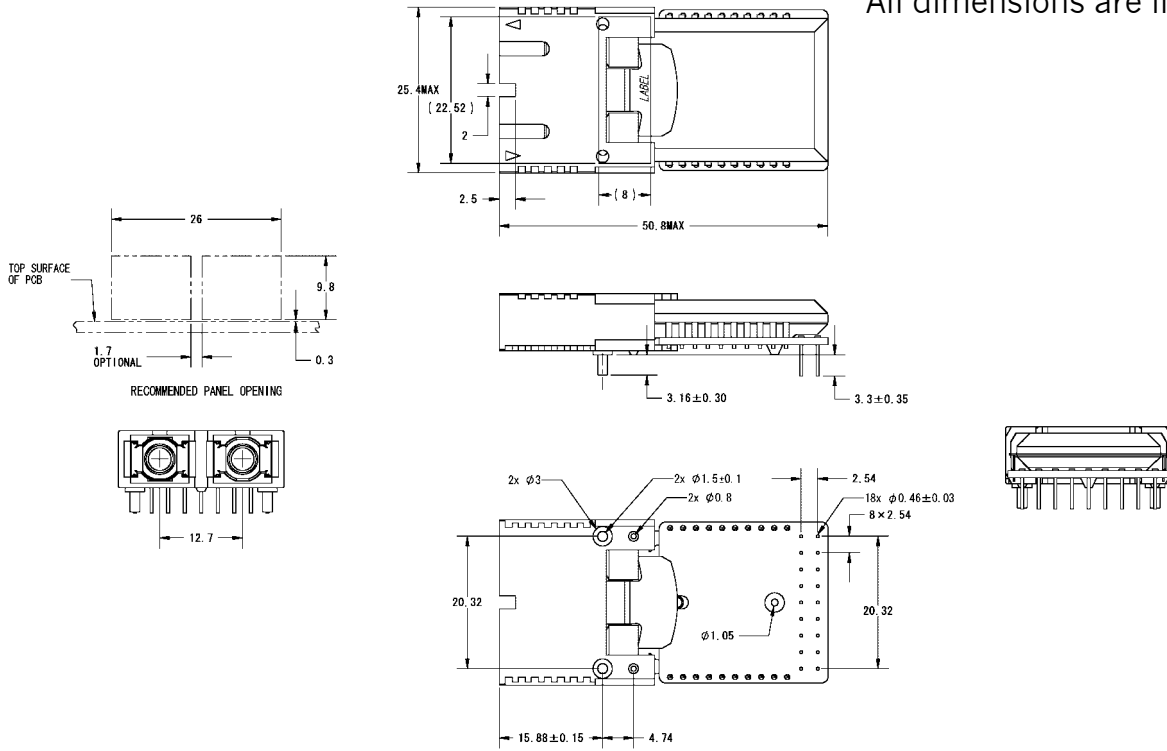


Figure2-2. Outline Dimensions (SDM7301-GC-ZN / SDM7301-GC-ZW)

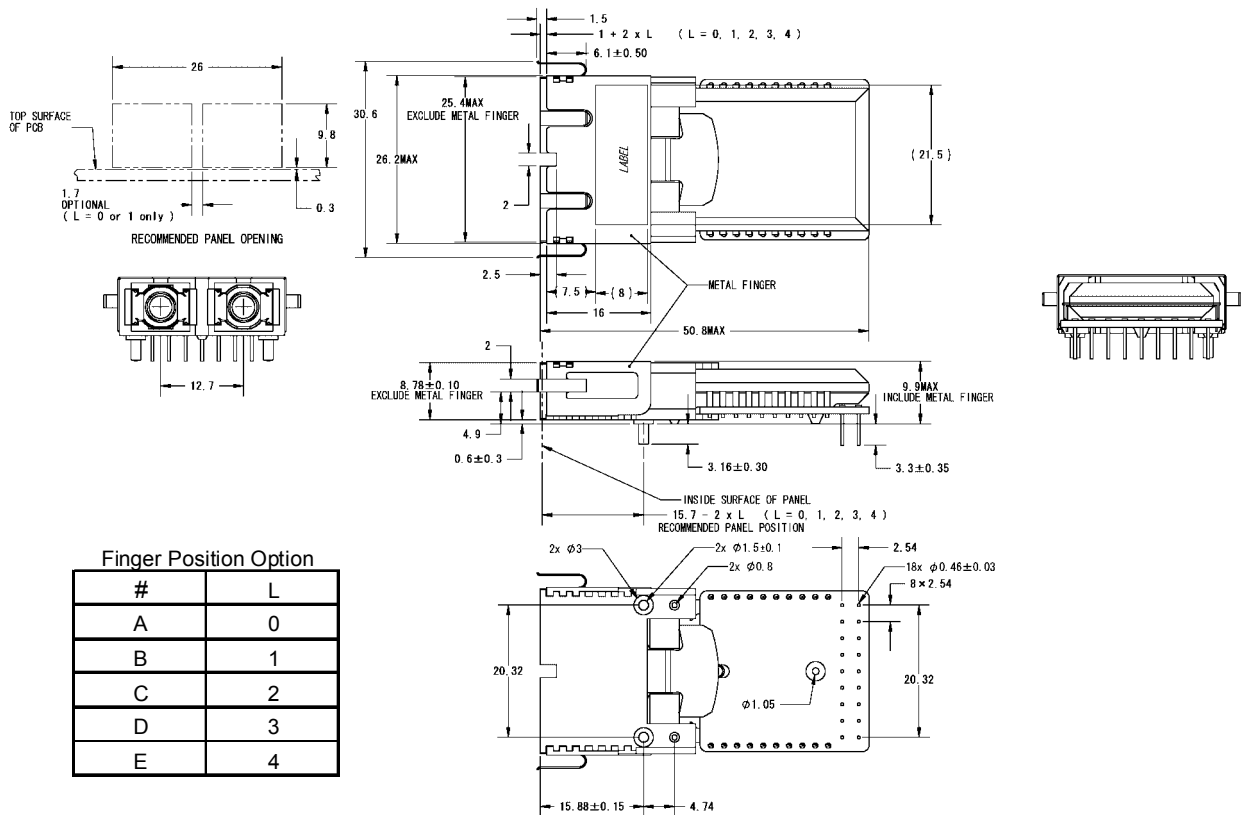


Figure2-3. Outline Dimensions (SDM7301-GC-#N / SDM7301-GC-#W)

(SDM7301-XC, SDM7301-GC)

## ⚠ Caution



Do not disassemble this product. Otherwise, failure, electrical shock, overheating or fire may occur.  
Handle the lead pins carefully. Use assisting tools or prospective aids as required. A lead pin may injure skin or human body

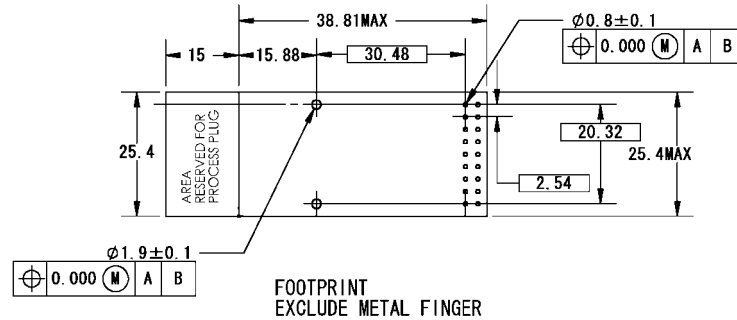


Figure2-4. Recommended Footprint

## 4. Pin Assignment

| No. | Symbol   | Function  |
|-----|----------|---|
| 1   | N/C      | Non Connection (Internally)                           |
| 2   | N/C      | Non Connection (Internally)                           |
| 3   | RCLKb    | Differential Clock Output (Negative)                  |
| 4   | RCLK     | Differential Clock Output (Positive)                  |
| 5   | BM(-)    | Monitoring pin for LD Bias Monitor                    |
| 6   | BM(+)    | Monitoring pin for LD Bias Monitor                    |
| 7   | Disable  | LD Disable Input                                      |
| 8   | RFM(+)   | Monitoring pin for Rear Facet Monitor                 |
| 9   | RFM(-)   | Monitoring pin for Rear Facet Monitor                 |
| 10  | Veetx    | Power Supply (-) for Transmitter : Connected to GND   |
| 11  | TD       | Transmitter Differential Data (Positive)              |
| 12  | TDb      | Transmitter Differential Data (Negative)              |
| 13  | Vcctx    | Power Supply (+) for Transmitter : Connected to +3.3V |
| 14  | Vccrx    | Power Supply (+) for Receiver : Connected to +3.3V    |
| 15  | FLAG(SD) | FLAG (Signal Detect)                                  |
| 16  | RDb      | Received Differential Data (Negative)                 |
| 17  | RD       | Received Differential Data (Positive)                 |
| 18  | Veerx    | Power Supply (-) for Receiver : Connected to GND      |

NC pins should left open for additional functions in the future

## 5. Absolute Maximum Ratings

| Parameter                              | Symbol  | min. | Max     | Unit | Note    |
|--|---------|------|---------|------|---------|
| Storage Case Temperature               | Ts      | -40  | 85      | °C   | 1, 2    |
| Operating Case Temperature             | Tc      | 0    | 70      | °C   | 1, 2, 3 |
|  |         | -40  | 85      | °C   | 1, 2, 4 |
| Supply Voltage                         | Vcc-Vee | 0.0  | 6.0     | V    | 5       |
| Input Voltage                          | Vi      | Vee  | Vcc+0.5 | V    | 6       |
| Lead Soldering (Temperature)<br>(Time) |         |      | 260     | °C   | 7       |
|  |         |      | 10      | sec. |         |

Note 1. No condensation allowed.

2. Don't remove or insert an optical connector except the environmental temperature at 0~70°C

3. SDM7301-XC 4. SDM7301-XC-W 5. Vcc>Vee

6. TD, TDb, Disable 7. Measured on lead pin at 2mm (0.079in.) off the package bottom

### ⚠ Warning

❗ Use the product with the rated voltage described in the specification. If the voltage exceeds the maximum rating, overheating or fire may occur.

### ⚠ Caution

⚠ Do not store the product in the area where temperature exceeds the maximum rating, where there is too much moisture or dampness, where there is acid gas or corrosive gas, or other extreme conditions. Otherwise, failure, overheating or fire may occur.

## 6. Electrical Interface

( Unless otherwise specified, Vcc-Vee = 4.75 to 5.25 V and all operating temperature shall apply. )

### 6-1. Transmitter side

| Parameter                     | Symbol  | min.    | Typ.     | Max.     | Unit  | Note |
|-------------------------------|---------|---------|----------|----------|-------|------|
| Supply Voltage                | Vcc-Vee | 4.75    | 5.00     | 5.25     | V     |      |
| Supply Current                | ldtx    |         | 70       | 150      | mA    | 1, 2 |
| Input Voltage<br>TD, TDb      | High    | Vih     | Vcc-1.17 | Vcc-0.73 | V     | 3, 4 |
|                               | Low     | Vil     | Vcc-1.95 | Vcc-1.45 |       |      |
| Input Current<br>TD, TDb      | High    | lih     | -10      | 150      | μA    | 3, 4 |
|                               | Low     | lil     | -10      | 10       |       |      |
| Signal Input Rise / Fall Time |         |         |          | 1.5      | nsec. | 5    |
| Disable Input Voltage         | Vdi     | Vee+2.0 |          | Vcc      | V     | 6    |
| Disable Input Current         | ldi     | -10     |          | 200      | μA    | 6    |
| LD Bias Monitor Voltage       | Vbm     | 0.01    |          | 0.50     | V     | 3, 7 |
| Rear Facet Monitor Voltage    | Vrfm    | 0.01    |          | 0.20     | V     | 2, 7 |

Note 1. Output current is not included. 2. Measured at the bit rate of 155.52Mbps, 50% duty cycle data, 3.Vcc=+5.0V, 4. Tc=25°C, 5. 20 ~ 80%

6. The transmitter is enable as default state and requires an external voltage only to disable.

7. The Laser Bias and Rear Facet Monitor currents are calculated as ratios of the corresponding voltages to their current-sensing resistors, 10Ω and 200Ω, respectively(refer to Figure 3). Upon measuring or utilizing these values, use a device whose input impedance is high enough(>1MΩ) compared with those resistors.

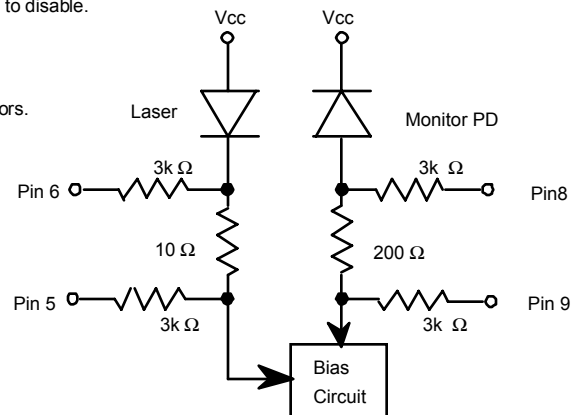


Figure 3 Monitor Circuit Schematic Diagram

### 6-2. Receiver side

| Parameter              | Symbol                            | min.            | Typ.                  | Max.                  | Unit  | Note |
|------------------------|-----------------------------------|-----------------|-----------------------|-----------------------|-------|------|
| Supply Voltage         | Vcc-Vee                           | 4.75            | 5.00                  | 5.25                  | V     |      |
| Supply Current         | I <sub>drx</sub>                  |                 | 145                   | 200                   | mA    | 1    |
| Data, Clock & FLAG     | High                              | V <sub>oh</sub> | V <sub>cc</sub> -1.26 | V <sub>cc</sub> -0.82 | V     | 2, 3 |
| Output Voltage         | Low                               | V <sub>ol</sub> | V <sub>cc</sub> -1.97 | V <sub>cc</sub> -1.54 | V     |      |
| Clock Rise / Fall Time | T <sub>rc</sub> / T <sub>fc</sub> |                 |                       | 700                   | psec. | 4, 5 |
| Data Rise / Fall Time  | T <sub>rd</sub> / T <sub>fd</sub> |                 |                       | 1000                  | psec. | 4, 5 |
| Clock Sampling Point   | T <sub>csp</sub>                  | 2.60            | 3.10                  | 3.60                  | nsec. | 6    |

Note 1. Output current is not included.

2. V<sub>cc</sub>=+5.0V, T<sub>c</sub>=25°C

3. Termination condition : R<sub>I</sub>= 50Ω to V<sub>ccrx</sub> =-2V

4. 20~80%

5. Input capacitance and stray capacitance of measuring devices should be less than 2pF

6. Phase difference between rising edge of RD and rising edge of RCLK. (Please refer to Figure 4)

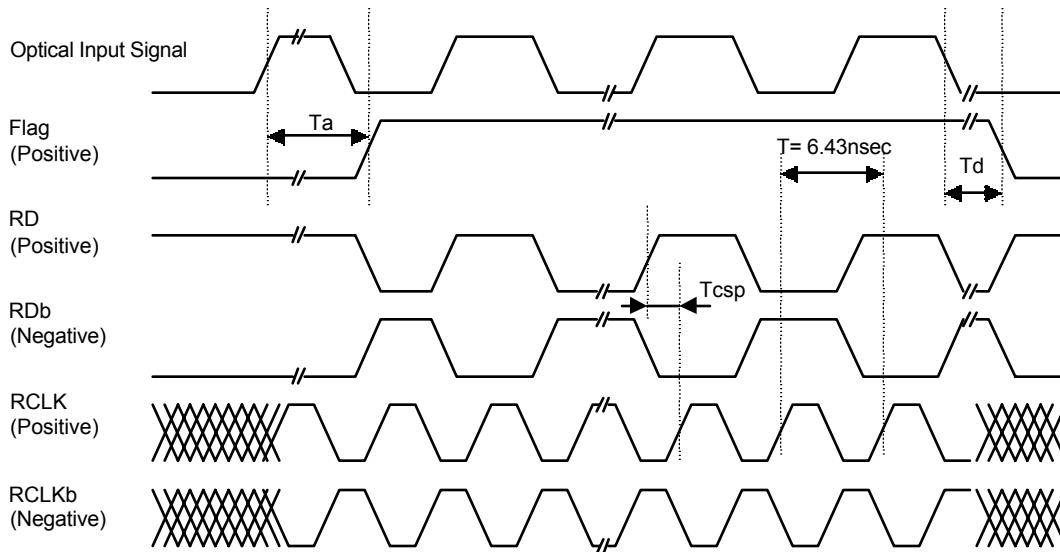


Figure 4. Timing Chart

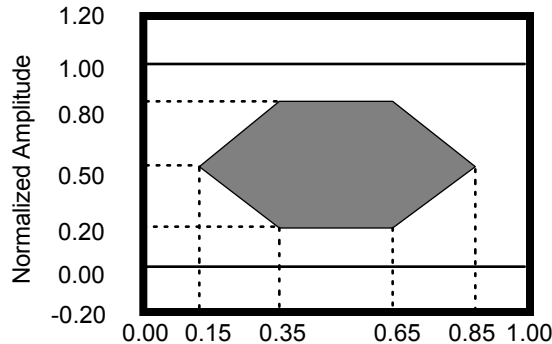
## 7. Optical Interface

( Unless otherwise specified, Vcc-Vee = 4.75 to 5.25 V and all operating temperature shall apply. )

### 7-1. Transmitter side

| Parameter                   | Symbol            | min.  | Typ. | Max. | Unit | Note |
|-----------------------------|-------------------|-------|------|------|------|------|
| Average Output Power        | Po                | -15.0 |      | -8.0 | dBm  | 1    |
| Extinction Ratio            | Er                | 8.2   |      |      | dB   | 1    |
| Center Wavelength           | $\lambda_c$       | 1261  |      | 1360 | nm   |      |
| Spectral Width (RMS)        | $\Delta\lambda$   |       |      | 7.7  | nm   |      |
| Eye Mask for Optical Output | Refer to Figure 5 |       |      |      |      |      |

Note 1. Measured at 155.52Mbps PRBS2<sup>23</sup>-1, 50% duty cycle data



Relation between Input Signal and Optical Output Signal

| Input Signal |      | Optical Output Signal |
|--------------|------|-----------------------|
| TD           | TDb  |                       |
| High         | Low  | ON (High)             |
| Low          | High | OFF (Low)             |
| High         | High | Undefined             |
| Low          | Low  | Undefined             |

Figure 5. Optical Pulse Mask with Fourth Order Bessel-Thomson Filter Specified in ITU-T G.957

### ⚠ Warning



Do not look at the laser beam projection area (e.g. end of optical connector) with naked eyes or through optical equipment while the power is supplied to this product. Otherwise, your eyes may be injured.

### 7-2. Receiver side

| Parameter                   | Symbol | min.             | Typ. | Max.  | Unit | Note    |
|-----------------------------|--------|------------------|------|-------|------|---------|
| Bit Rate Range              | -      | 155.52 +/- 50ppm |      |       | Mbps |         |
| Minimum Sensitivity         | Pmin   |                  |      | -28.0 | dBm  | 1, 2    |
| Overload                    | Pmax   | -8.0             |      |       | dBm  | 1, 2    |
| Clock Jitter (rms)          | Tjc    |                  |      | 64    | psec | 2, 3, 4 |
| Clock Duty                  | Cduty  | 45.0             | 50.0 | 55.0  | %    | 2, 3, 4 |
| Consecutive Identical Digit | CID    | 72               | 100  |       | bits | 5       |
| SD Assert Level             | Pa     | -48              | -37  | -28   | dBm  | 2       |
| SD deassert Level           | Pd     | -49              | -39  | -28   | dBm  |         |
| SD Assert time              | Ta     |                  |      | 100   | μsec | 2, 3, 6 |
| SD deassert Time            | Td     |                  |      | 100   | μsec |         |

Note 1. BER=10<sup>-10</sup>

2. Measured at the bit rate of 155.52Mbps, PRBS 2<sup>23</sup>-1, NRZ

3. Optical Input Power: -28.0 ~ -8.0dBm

4. Termination condition for RD, RDb, RCLK, RCLKb : RI=50Ω to Vccrx-2V

5. Duty 50% input signal

6. Refer to Figure 4

## 8. Relation between Disable Input Voltage and Optical Output Power

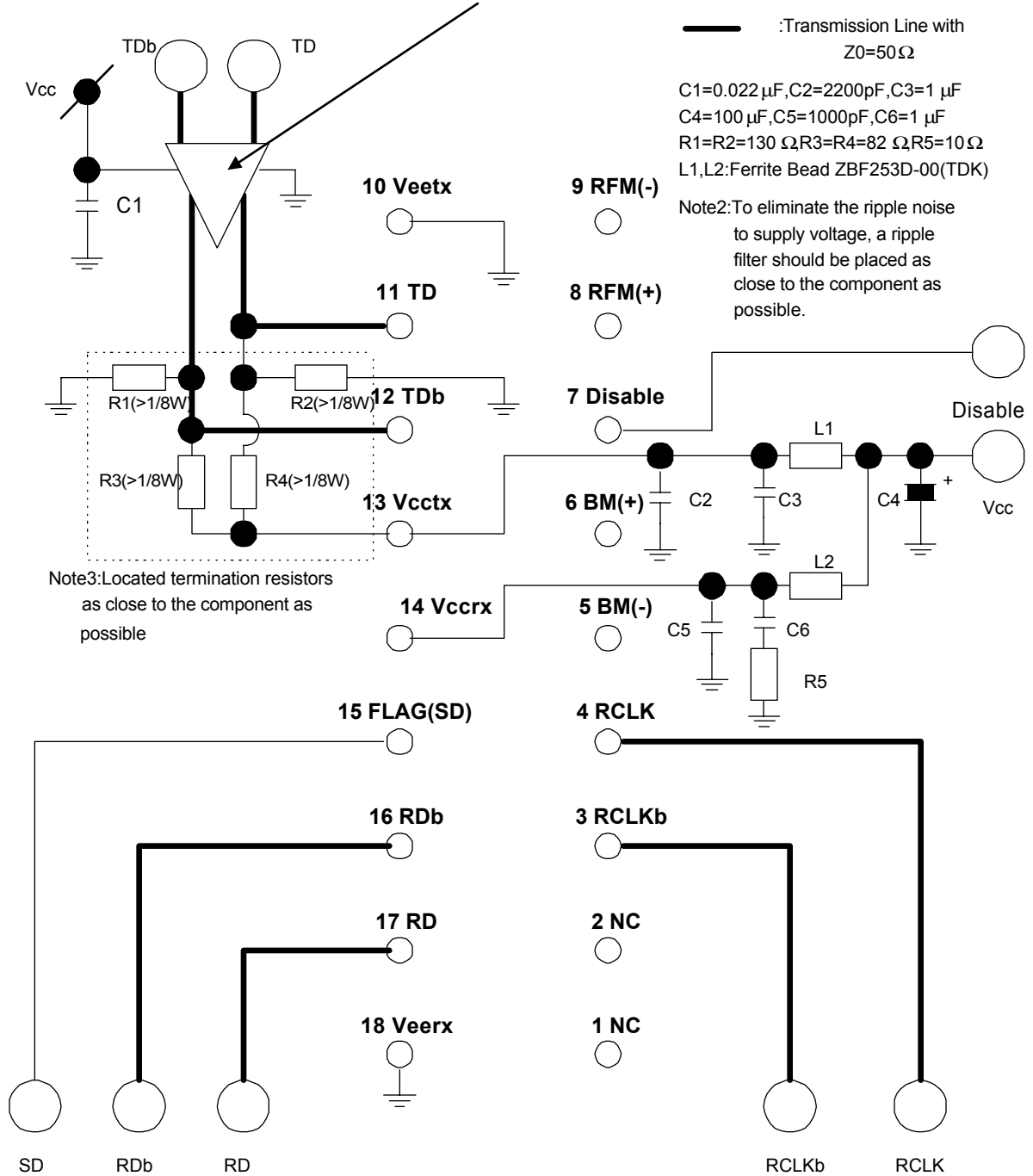
| Disable Input Voltage             | Optical Output Power |
|-----------------------------------|----------------------|
| "L" (0[V] ~ 0.8[V])               | Enabled              |
| "H" (2.0[V] ~ VCC <sub>TX</sub> ) | Disabled (<-45dBm)   |

Note. Enabled for no Disable input (pin 7 opened)

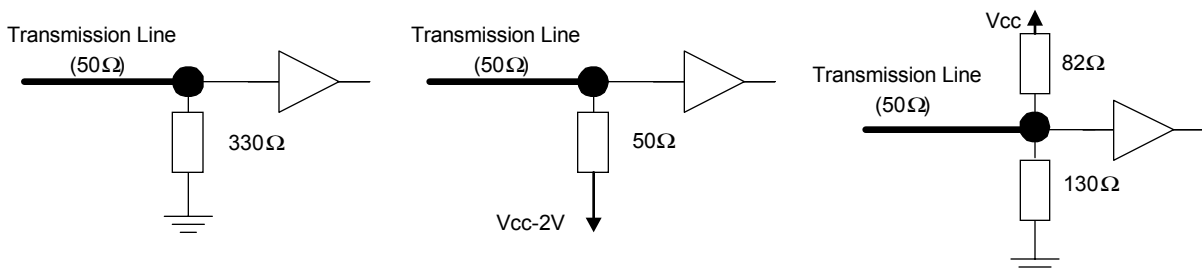


## 9.Recommended Interface Circuit

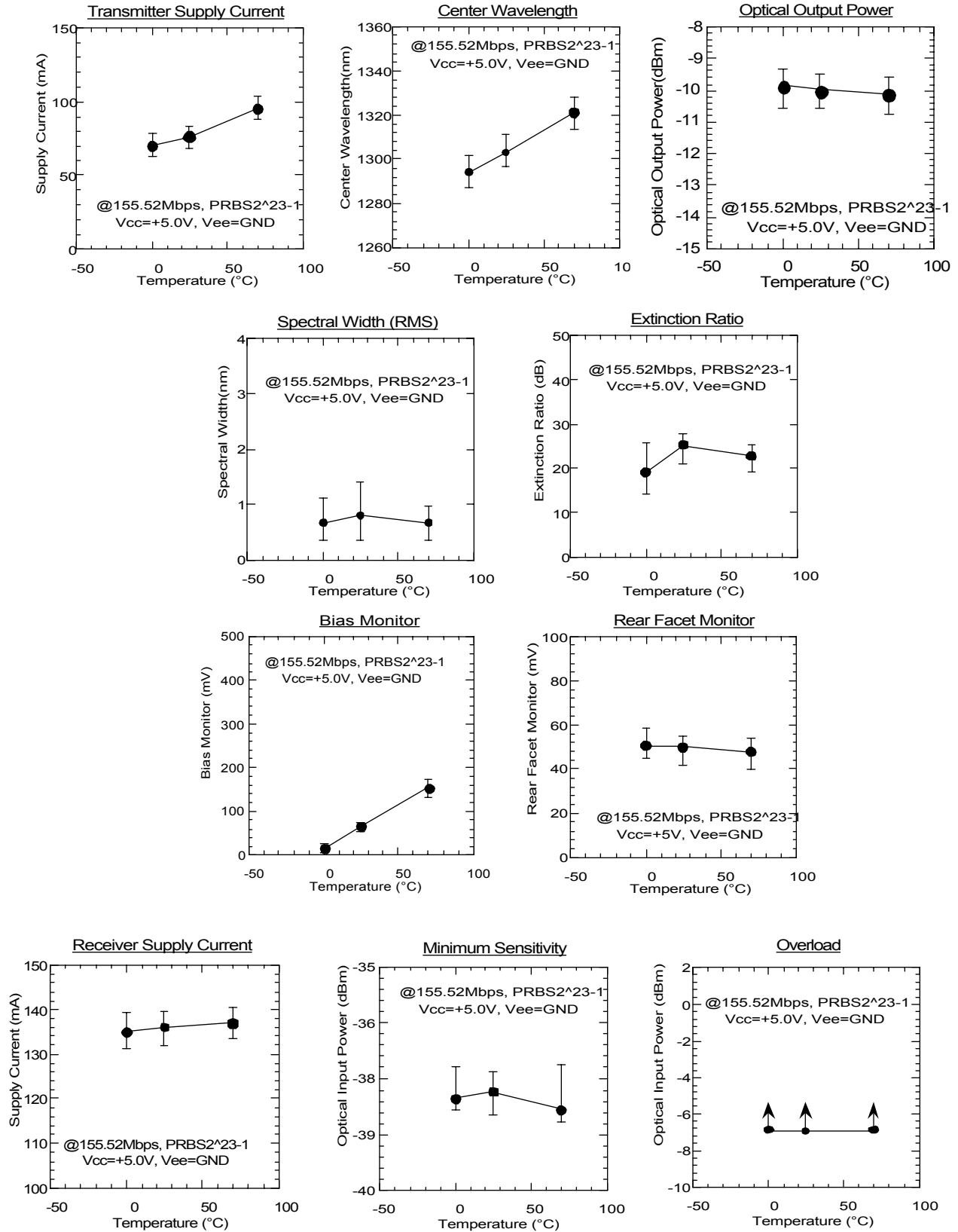
Note1: This IC should be designed just before the Transmitter input Terminals to reduce input signal's jitter.



Output signal (RD, RDb, RCLK, RCLKb) are open emitter, so terminated condition as follows



## 10. Characteristic Information

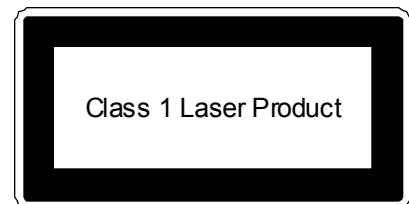


## 11. Reliability Test

| Bellcore TA-NWT-000983 Issue 2, December 1993 |                                |                          |                            |                 |     |     |            |     |   |
|---|--------------------------------|--------------------------|----------------------------|-----------------|-----|-----|------------|-----|---|
| Heading                                       | Test                           | Reference                | Condition                  | Sampling        |     |     | SEI Result |     |   |
|   |                                |                          |                            | LTPD            | SS  | C   | SS         | F/C |   |
| Mechanical Integrity                          | Mechanical Shock               | MIL-STD-883 Method 2002  | Condition B                |                 |     |     |            |     |   |
|   |                                |                          | 5 times/axis               | 20%             | 11  | 0   | ---        | --- |   |
|   |                                |                          |                            | 500G, 1.0 ms    | 20% | 11  | 0          | 11  | 0 |
|   |                                |                          |                            | 1,500G, 0.5ms   |     |     |            |     |   |
|   | Vibration                      | MIL-STD-883 Method 2007  | Condition A                | 20%             | 11  | 0   | 11         | 0   |   |
|   |                                |                          | 20 G                       |                 |     |     |            |     |   |
|   |                                |                          | 20-2,000 Hz                |                 |     |     |            |     |   |
|   |                                |                          | 4 min/cycle; 4 cycles/axis |                 |     |     |            |     |   |
|   | Thermal Shock                  | MIL-STD-883 Method 1011  | ΔT=100°C                   | 20%             | 11  | 0   | 11         | 0   |   |
|   | Solderability                  | MIL-STD-883 Method 2003  | (steam aging not required) | 20%             | 11  | 0   | 11         | 0   |   |
|   | Fiber Pull                     |                          | 1 Kg; 3 times;5sec.        | 20%             | 11  | 0   | ---        | --- |   |
|   |                                |                          | 2 Kg; 3 times; 5sec.       | 20%             | 11  | 0   | ---        | --- |   |
| Endurance                                     | Accel. Aging (High Temp.)      | (R)-453 Section 5.18     | +85°C; rated power         | ---             | 25  | --- | 25         | 0   |   |
|   |                                |                          | >5,000hrs.                 | ---             | 10  | --- | ---        | --- |   |
|   |                                |                          |                            | >10,000hrs.     |     |     |            |     |   |
|   | High Temp. Storage             | -----                    | max. storage T (T=85°C)    | 20%             | 11  | 0   | ---        | --- |   |
|   |                                |                          | >2,000                     |                 |     |     |            |     |   |
|   | Low Temp. Storage              | -----                    | min. storage T (T=-40°C)   | 20%             | 11  | 0   | 11         | 0   |   |
|   |                                |                          | >2,000                     |                 |     |     |            |     |   |
|   | Temperature Cycling            | Section 5.20             |                            | - 40°C to +85°C |     |     |            |     |   |
| 400 times pass/fail                           |                                |                          |                            | 20%             | 11  | 0   | ---        | --- |   |
| 500 times for info.                           |                                |                          |                            | ---             | 11  | --- | ---        | --- |   |
| 500 times pass/fail                           |                                |                          |                            | 20%             | 11  | 0   | 11         | 0   |   |
|   |                                |                          | 1000 times for info.       | ---             | 11  | --- | 11         | 0   |   |
| Damp Heat (if using epoxy)                    | MIL-STD-202 M103 or IEC 68-2-3 | 40°C , 95%, 56days       | 20%                        | 11              | 0   | 11  | 0          |     |   |
|   |                                | or 85°C /85%RH 2,000hrs. | 20%                        | 11              | 0   | --- | ---        |     |   |
| Cyclic Moisture Resistance                    | Section 5.23                   | -----                    | 20%                        | 11              | 0   | 11  | 0          |     |   |
| Special Tests                                 | Internal Moisture              | MIL-STD-883 Method 1018  | < 5,000 ppm water vapor    | 20%             | 11  | 0   | 11         | 0   |   |
|   | Flammability                   | TR357:Sec. 4.4.2.5       | -----                      | ---             | --- | --- | ---        | OK  |   |
|   | ESD Threshold                  | Section 5.22             | -----                      | ---             | 6   | --- | 6          | 0   |   |

## 12. Laser Safety

This product uses a semiconductor laser system and is a laser class 1 product acc. FDA, complies with 21CFR1040. 10 and 1040.11. Also this product is a laser class 1 product acc. IEC 825-1.



### ⚠ Caution



If this product is used under conditions not recommended in the specification or this product is used with unauthorized revision, classification for laser product safety standard is invalid. Classify the product again at your responsibility and take appropriate actions.

### 13. Ordering Information

| Ordering Number | Connector type      | Operating Temperature |
|-----------------|---------------------|-----------------------|
| SDM7301-XC      | SC Duplex Connector | 0 ~ 70°C              |
| SDM7301-XC-W    | SC Duplex Connector | -40 ~ 85°C            |
| SDM7301-GC-##   | See chart below     |                       |

SDM7301-GC-##

└ Operating Case Temperature Option

N : 0°C ~ 70°C

W : -40°C ~ 85°C

└ EMI Shield Finger Option

Z : Without Finger

A ~ E : With Finger

(Letter specifies finger position. Refer to Figure2-3 for detail.)

### 14. Other Precaution

Under such a strong vibration environment as in automobile, the performance and reliability are not guaranteed.

The governmental approval is required to export this product to other countries. To dispose of these components, the appropriate procedure should be taken to prevent illegal exportation.

This module must be handled, used and disposed of according to your company's safe working practice.

#### ⚠ Warning



Be sure to carry out correct soldering for connection to peripheral circuits in order to prevent contact failure or short-circuit. Otherwise, a strong laser beam may cause eye injury, overheating or fire.



Do not put this product or components of this product into your mouth. This product contains material harmful to health.

#### ⚠ Caution



Be sure to turn the power off when you touch this product connected to the printed circuit boards. Otherwise, electric shock may occur.



Dispose this product or equipment including this product properly as an industrial waste according to the regulations.

### 15. For More Information

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<http://www.sei.co.jp/Electro-optic/index.html>

(SDM7301-XC, SDM7301-GC)