



# SIDC42D120E

## Fast switching diode chip in EMCON-Technology

### **FEATURES:**

- 1200V EMCON technology 130 µm chip
- soft, fast switching
- low reverse recovery charge
- small temperature coefficient

## This chip is used for:

EUPEC power modules and discrete devices



### **Applications:**

SMPS, resonant applications, drives

Chip Type	V <sub>CE</sub>	I <sub>Cn</sub>	Die Size	Package	Ordering Code	
SIDC42D120E	1200V	50A	6.50 x 6.50 mm <sup>2</sup>	sawn on foil	C67047-A4680	

### **MECHANICAL PARAMETER:**

Raster size	6.50 x 6.50				
Area total / active	42.25 / 33.99	mm <sup>2</sup>			
Anode pad size	5.08 x 5.08				
Thickness	130	μm			
Wafer size	125	mm			
Flat position	180	deg			
Max. possible chips per wafer	226				
Passivation frontside	Photoimide				
Anode metalization	3200 nm Al Si 1%				
Cathode metalization	athode metalization 1400 nm Ni Ag –system suitable for epoxy and soft solder die bond				
Die bond	electrically conductive glue or solder				
Wire bond	Al, ≤500μm				
Reject Ink Dot Size	Dot Size tbd				
Recommended Storage Environment	store in original container, in dry nitrogen, < 6 month				



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# **Maximum Ratings**

Parameter	Symbol	Condition	Value	Unit
Repetitive peak reverse voltage	$V_{RRM}$		1200	V
Continous forward current limited by	1_		50	
T <sub>jmax</sub>	/ <sub>F</sub>		30	А
Single pulse forward current	I <sub>FSM</sub>	$t_P = 10 \text{ ms sinusoidal}$	tbd	
(depending on wire bond configuration)	1 F 2 IVI	tp = 10 mo omaocidar	tod	
Maximum repetitive forward current	1		100	
limited by T <sub>jmax</sub>	I <sub>FRM</sub>		100	
Operating junction and storage temperature	$T_{\rm j}$ , $T_{ m stg}$		-55+150	°C

# Static Electrical Characteristics (tested on chip), $T_j$ =25 °C, unless otherwise specified

Parameter	Symbol	Conditions		Value			Unit
- arameter	Syllibol			min.	Тур.	max.	] 01111
Reverse leakage current	$I_{R}$	V <sub>R</sub> =1200V	<i>T<sub>j</sub></i> =25 °C			250	μΑ
Cathode-Anode breakdown Voltage	V <sub>Br</sub>	I <sub>R</sub> =4mA	<i>T<sub>j</sub></i> =25°C	1200			V
Forward voltage drop	V <sub>F</sub>	I <sub>F</sub> =50A	<i>T<sub>j</sub></i> =25°C		1.90		V

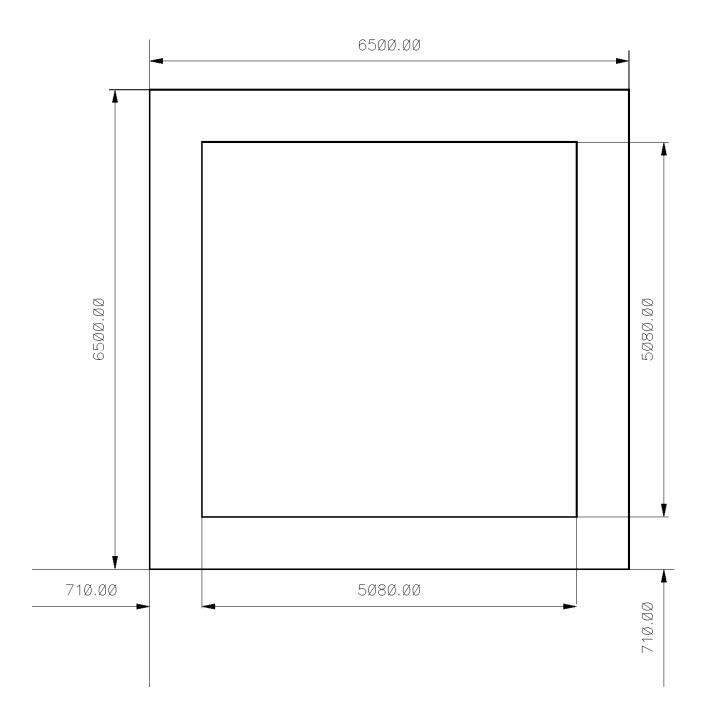
# **Dynamic Electrical Characteristics**, at $T_j$ = 25 °C, unless otherwise specified, tested at component

Daramatar	Symbol	Canditions		Value			Limit	
Parameter	Symbol Conditions			min.	Тур.	max.	Unit	
Reverse recovery time	t <sub>rr1</sub>	I <sub>F</sub> =50A	$T_j = 25  ^{\circ}C$		tbd			
	t <sub>rr2</sub>	$di/dt=1300A/ms$ $V_R=600V$	$T_j = 150  ^{\circ}\text{C}$				ns	
Peak recovery current	I <sub>RRM1</sub>	I <sub>F</sub> =50A	$T_j = 25  ^{\circ}\text{C}$		52		_	
	I <sub>RRM2</sub>	$di/dt=1300A/ms$ $V_R=600V$	$T_j = 150  ^{\circ}\text{C}$		66		T A	
Reverse recovery charge	Q <sub>rr1</sub>	I <sub>F</sub> =50A	T <sub>j</sub> =25°C		5.1		nC	
	Q <sub>rr2</sub>	$di/dt=1300A/ms$ $V_R=600V$	T <sub>j</sub> =150°C		10.7			
Peak rate of fall of reverse recovery current	di <sub>rr1</sub> /dt	I <sub>F</sub> =50A	T <sub>j</sub> =25°C		tbd			
	di <sub>rr2</sub> /dt	di/dt = 1300A/ms $V_R = 600V$	T <sub>j</sub> =150°C				A/μs	
Softness	S1	I <sub>F</sub> =50A	T <sub>j</sub> =25°C		tbd		1	
	S2	$V_{R} = 600V$	T <sub>j</sub> =150°C					



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## **CHIP DRAWING:**





### **Preliminary**

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#### **FURTHER ELECTRICAL CHARACTERISTICS:**

This chip data sheet refers to the device data sheet refers to the data sheet refer

### **Description:**

AQL 0,65 for visual inspection according to failure catalog

Electrostatic Discharge Sensitive Device according to MIL-STD 883

Test-Normen Villach/Prüffeld

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