

SPEC. NO.	TQ3C-8EAC0-E1AAZS19-00
DATE	September 1, 1999

S P E C

10,4 inch STN
transflective,
SVGA color
RT4060

FOR: _____

TYPE : KHB104SV1AA-G81

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KYOCERA CORPORATION
KAGOSHIMA HAYATO PLANT
LCD DIVISION

This specification is subject to change without notice.
Consult Kyocera before ordering.

Original	Designed by :Engineering Dept.			Confirmed by :QA Dept.	
Issue Data	Prepared	Checked	Approved	Checked	Approved
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Revision Record

Date	Designed by : Engineering Dept.			Confirmed by : QA Dept.	
	Prepared	Checked	Approved	Checked	Approved
Rev. No.	Date	Page	Descriptions		

1. Application

This data sheet defines the specification for a $(800 \times 3) \times 600$ dot, STN transfective color dot matrix type Liquid Crystal Display with CFL backlight.

2. Construction and Outline

$(800 \times 3) \times 600$ dots, COB type LCD with CFL backlight.

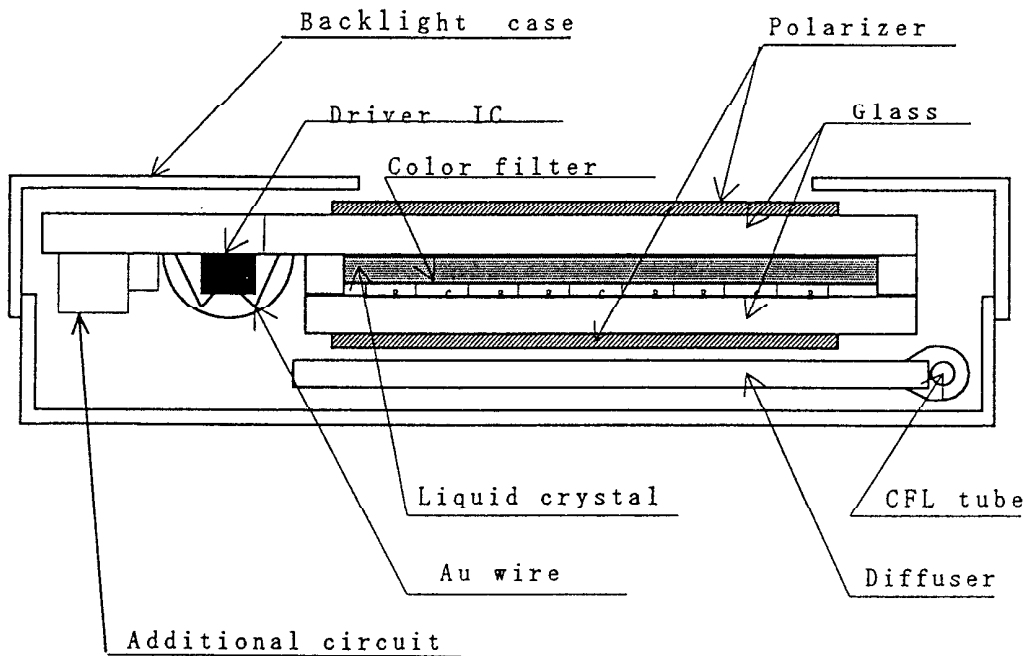
Backlight system : Side-edge type CFL (1 tube).

Inverter : Option.

Recommended Inverter : PH BLC08 K2 (HITACHI MEDIA ELECTRONICS),
or equivalent

Polarizer : Glare treatment.

Additional circuit : Bias voltage circuit, Randomizing circuit, DC/DC converter.



3. Mechanical Specifications

ITEM	SPECIFICATION	UNIT
Outline dimensions	264.0 (W) × 183.0 (H) × 8.5 (D)	mm
Effective viewing area	214.18 (W) × 161.38 (H)	mm
Dot number	(800×3) (W) × 600 (H)	Dots
Dot size	0.068 (W) × 0.244 (H)	mm
Dot pitch	0.088 (W) × 0.264 (H)	mm
Display color *1	White *2	—
Base color *1	Black *2	—
Weight	540	g

*1 Due to the characteristics of the LC material, the color vary with environmental temperature.

*2 Negative-type display

Display data "H" :R, G, B Dots ON : White

Display data "L" :R, G, B Dots OFF : Black

4. Absolute Maximum Ratings

4-1 Electrical absolute maximum ratings

Temp. = 25 °C

ITEM	SYMBOL	MIN.	MAX.	UNIT
Supply voltage for logic	VDD	0	6.0	V
Supply voltage for LCD driving	VCONT	0	VDD	V
Input voltage	Vin	0	VDD+0.3	V

4-2 Environmental absolute maximum ratings

ITEM	SYMBOL	MIN	MAX	UNIT
Operating temperature *1	Top	0	50	°C
Storage temperature *2	T _{STO}	-20	60	°C
Operating humidity *3	H _{OP}	10	*4	%RH
Storage humidity *3	H _{STO}	10	*4	%RH
Vibration	—	*5	*5	—
Shock	—	*6	*6	—

*1 LCD's display quality shall not be guaranteed at the temperature range of below 0°C and upper 40°C.

*2 Temp. = -20°C < 24 Hr. , Temp = 60°C < 24 Hr.
No vibration and shock.

*3 Non-condensation.

*4 Temp. ≤ 40°C, 85% RH Max.
Temp. > 40°C, Absolute Humidity shall be less than 85%RH at 40°C.

*5

Frequency	10~55 Hz	Converted to acceleration value : (0.03~0.91G)
Vibration width	0.15 mm	
Interval	10-55-10 Hz 1 minute	

2 hours in each direction X/Y/Z (6 hours as total)
EIAJ ED-2531.

*6 Acceleration: 50 G
Pulse width : 11 msec
3 times in each direction : ±X/±Y/±Z.
EIAJ ED-2531.

5. Electrical Characteristics

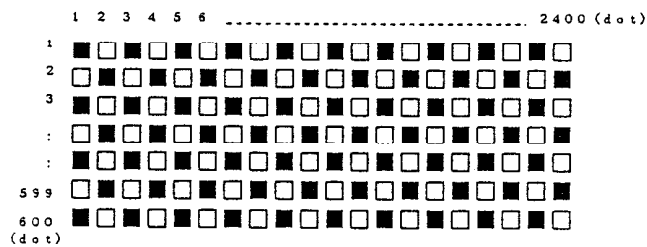
Temp. = 25°C, VDD = +5.0V ± 5%

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
Supply voltage for logic	VDD	—	4.75	5.00	5.25	V
LCD driving voltage *1	Vop= VCONT	0 °C	0.80	—	—	V
		25 °C	1.35	1.95	2.55	V
		50 °C	—	—	2.80	V
Input voltage	Vin	"H" level	0.8VDD	—	VDD	V
		"L" level	0	—	0.2VDD	V
Clock frequency	f _{CP}		—	6.75	18.0	MHz
Frame frequency *2	f _{FRM}		—	75	—	Hz
Current consumption for logic	IDD	*3	—	(110)	(165)	mA
	Pdisp		—	(550)	(825)	mW

*1 Maximum contrast ratio is obtained by adjusting the LCD supply voltage (Vop= VCONT) for driving LCD.

*2 In consideration of display quality, it is recommended that frame frequency is set in the range of 70-80Hz. When you have to use higher frame and clock frequencies, confirm the LCD's performance and quality prior to finalizing the frequency values: Generally, as frame and clock frequencies become higher, current consumption will get bigger and display quality will be degraded.

*3 Display high frequency pattern, (see below).
VDD = 5.0V , Vop = VCONT , f_{FRM} = 75 Hz , f_{CP} = 6.75MHz
Pattern:



6. Optical Characteristics

6-1. Reflective mode

Measuring Spot = ϕ 6mm , Temp. = 25°C

ITEM		SYMBOL	CONDITION		MIN.	TYP.	MAX.	UNIT
Response time	Rise	Tr	$\theta = \phi = 0^\circ$		—	(250)	(350)	ms
	Down	Td	$\theta = \phi = 0^\circ$		—	(200)	(300)	ms
Viewing angle range		θ	CR \geq 2	$\phi = 0^\circ$	(-60)	—	(60)	deg.
		ϕ		$\theta = 0^\circ$	(-45)	—	(45)	deg.
Contrast ratio		CR	$\theta = \phi = 0^\circ$		(3.0)	(5.0)	—	—
Reflectance		ρ	—		(TBD)	(TBD)	—	%
Chromaticity coordinates	Red	x	$\theta = \phi = 0^\circ$		(TBD)	(TBD)	(TBD)	—
		y			(TBD)	(TBD)	(TBD)	
	Green	x	$\theta = \phi = 0^\circ$		(TBD)	(TBD)	(TBD)	
		y			(TBD)	(TBD)	(TBD)	
	Blue	x	$\theta = \phi = 0^\circ$		(TBD)	(TBD)	(TBD)	
		y			(TBD)	(TBD)	(TBD)	
	White	x	$\theta = \phi = 0^\circ$		(TBD)	(TBD)	(TBD)	
		y			(TBD)	(TBD)	(TBD)	

Optimum contrast is obtained by adjusting the LCD driving voltage(Vop) while at the viewing angle of $\theta = \phi = 0^\circ$.

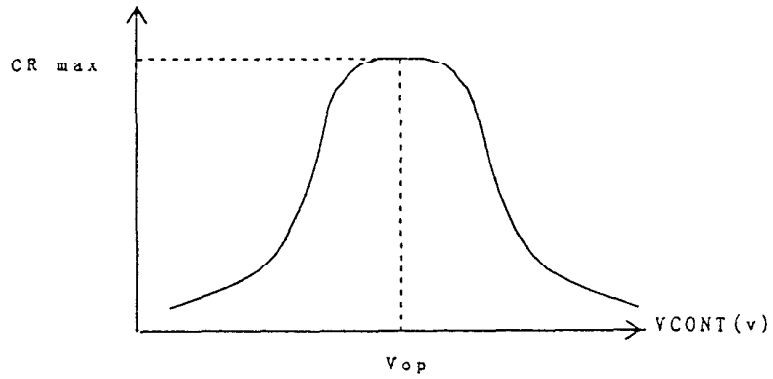
6-2. Transmissive mode

Measuring Spot = ϕ 6mm , Temp. = 25°C

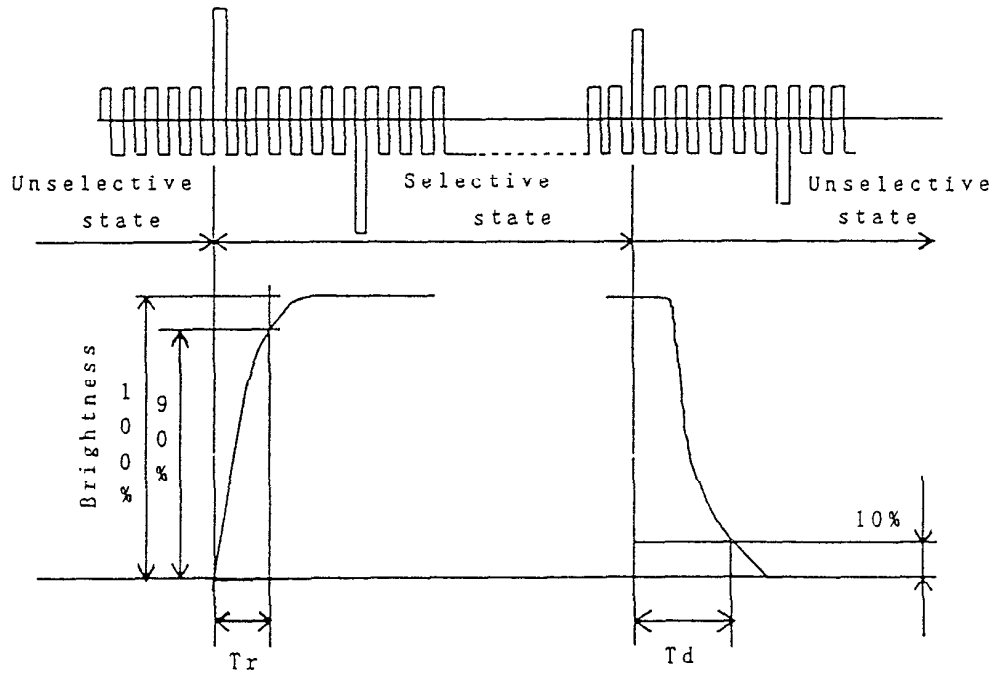
ITEM		SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	
Response time	Rise	Tr	$\theta = \phi = 0^\circ$	—	(250)	(350)	ms	
	Down	Td	$\theta = \phi = 0^\circ$	—	(200)	(300)	ms	
Viewing angle range		θ	CR \geq 2	$\phi = 0^\circ$	(-40)	—	(40)	deg.
		ϕ		$\theta = 0^\circ$	(-40)	—	(35)	deg.
Contrast ratio		CR	$\theta = \phi = 0^\circ$	(8.0)	(16.0)	—	—	
Brightness		L	—	(40)	(60)	—	cd/m ²	
Chromaticity coordinates	Red	x	$\theta = \phi = 0^\circ$	(TBD)	(TBD)	(TBD)	—	
		y		(TBD)	(TBD)	(TBD)		
	Green	x	$\theta = \phi = 0^\circ$	(TBD)	(TBD)	(TBD)		
		y		(TBD)	(TBD)	(TBD)		
	Blue	x	$\theta = \phi = 0^\circ$	(TBD)	(TBD)	(TBD)		
		y		(TBD)	(TBD)	(TBD)		
	White	x	$\theta = \phi = 0^\circ$	(TBD)	(TBD)	(TBD)		
		y		(TBD)	(TBD)	(TBD)		

Optimum contrast is obtained by adjusting the LCD driving voltage(Vop) while at the viewing angle of $\theta = \phi = 0^\circ$.

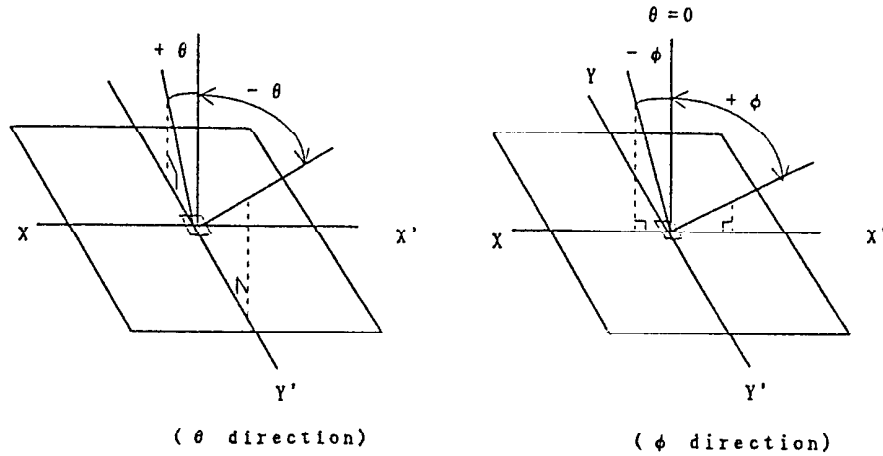
6-3. Definition of V_{op}



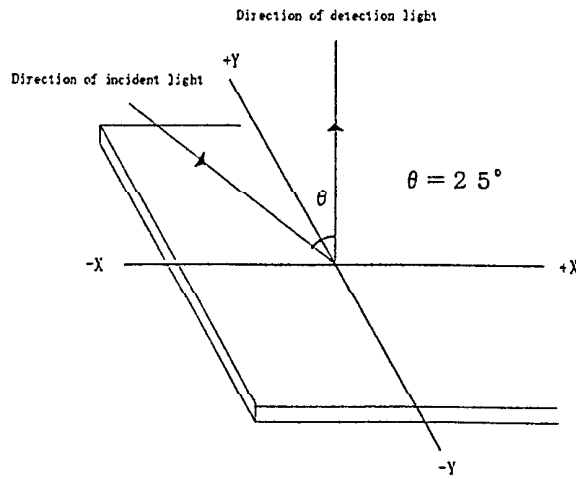
6-4. Definition of response time



6-5. Definition of viewing angle



6-6. Measurement method of reflectance



6-7. Definition of Contrast (Reflective Mode)

$$CR(\text{Contrast}) = \frac{\text{Reflection Brightness at all pixels "White"}}{\text{Reflection Brightness at all pixels "Black"}}$$

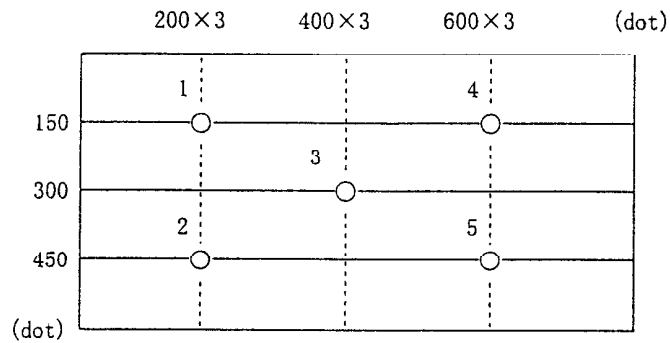
6-8. Definition of Contrast (Transmissive Mode)

$$CR(\text{Contrast}) = \frac{\text{Brightness at all pixels "White"}}{\text{Brightness at all pixels "Black"}}$$

6-9. Definition of Reflectance:

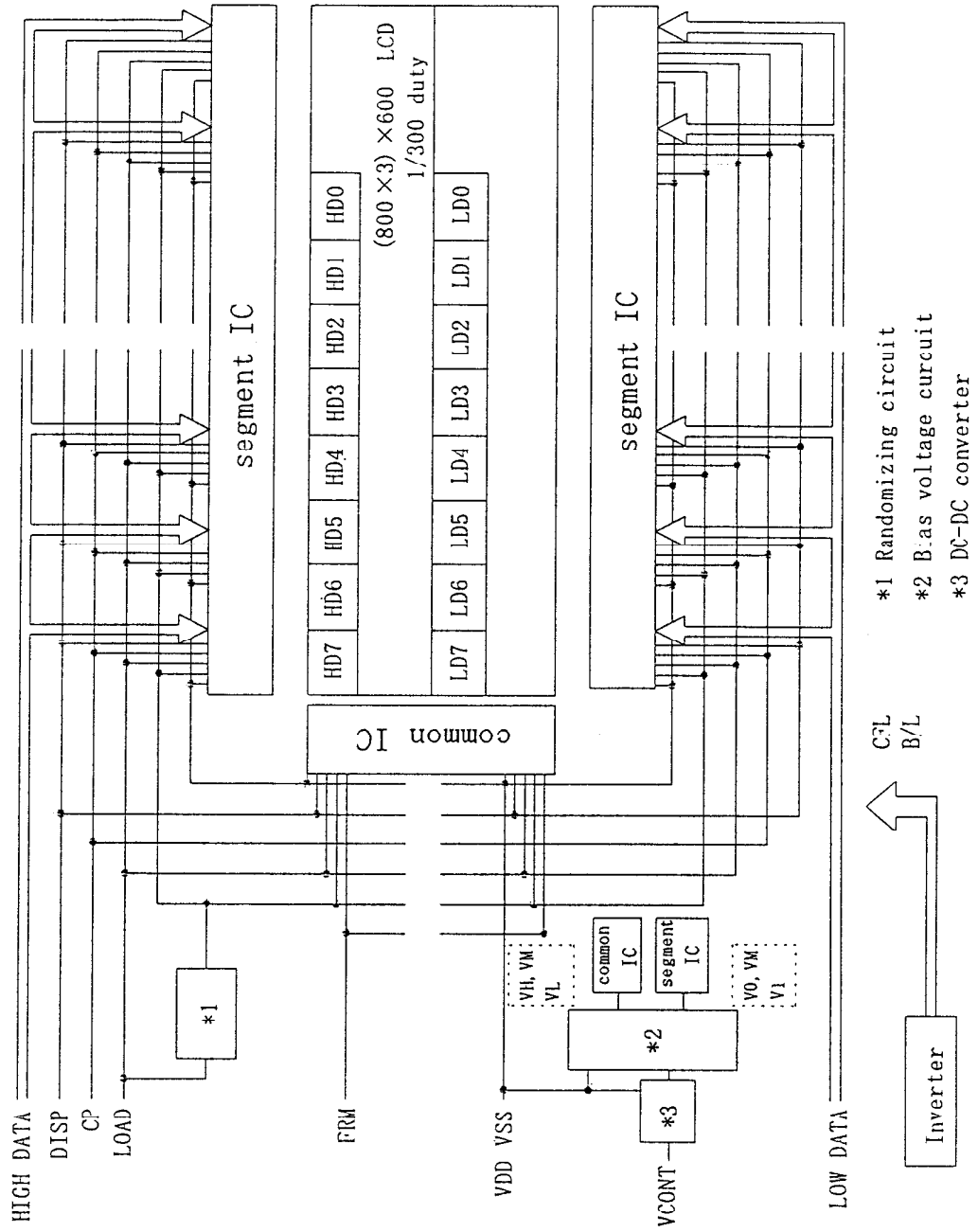
$$\rho \text{ (Reflectance)} = \frac{\text{Measured Reflection Brightness}}{\text{Reflection Brightness against Standard White Board}} \times 100(\%)$$

6-10. Measuring points

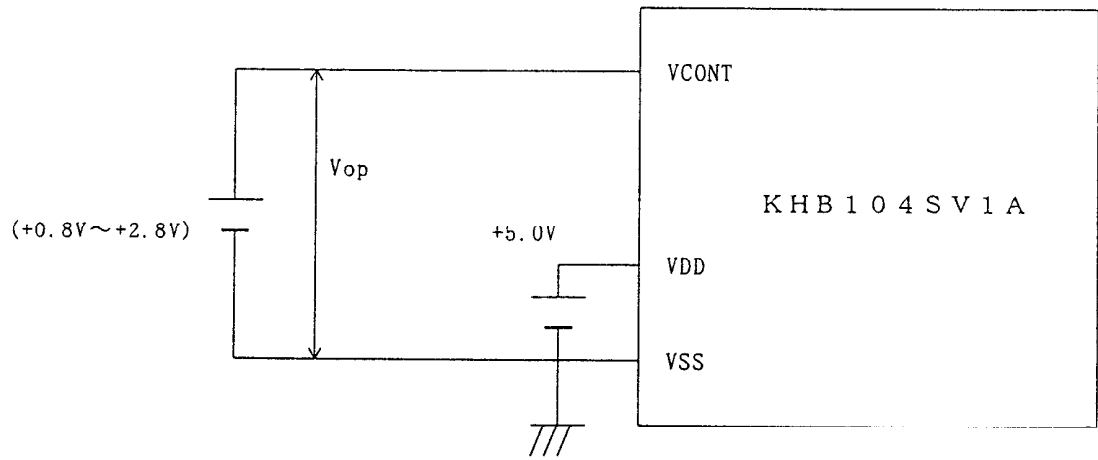


- 1) Rating is defined as the average brightness inside the viewing area.
- 2) 30 minutes after CFL is turned on. (Ambient Temp. =25°C)
- 3) The inverter should meet the eccentric conditions;
-Sine, symmetric waveform without spike in positive and negative.

7. Circuit Block Diagram



7-1. Power Supply



8. Interface Signals

8-1. LCD

CN1 : 53261-1510(Molex)

PIN NO.	SYMBOL	DESCRIPTION	LEVEL
1	FRM	Synchronous signal for driving scanning line	H
2	NC	No connect	—
3	DISP	Display control signal	H(ON), L(OFF)
4	LOAD	Data signal latch clock	H → L
5	VSS	GND	—
6	CP	Data signal shift clock	H → L
7	VSS	GND	—
8	HD0	Display data (Upper column)	H(ON), L(OFF)
9	HD1		
10	HD2		
11	HD3		
12	HD4		
13	HD5		
14	HD6		
15	HD7		

Recommended matching connector : 51021-1500(Molex)

CN2 : 53261-1410(Molex)

PIN No	SYMBOL	DESCRIPTION	LEVEL
1	LD0	Display data (Lower column)	H(ON), L(OFF)
2	LD1		
3	LD2		
4	LD3		
5	LD4		
6	LD5		
7	LD6		
8	LD7		
9	VDD	Power supply for logic	—
10	VSS	GND	—
11	NC	No connect	—
12	NC	No connect	—
13	NC	No connect	—
14	VCONT	LCD adjust voltage	—

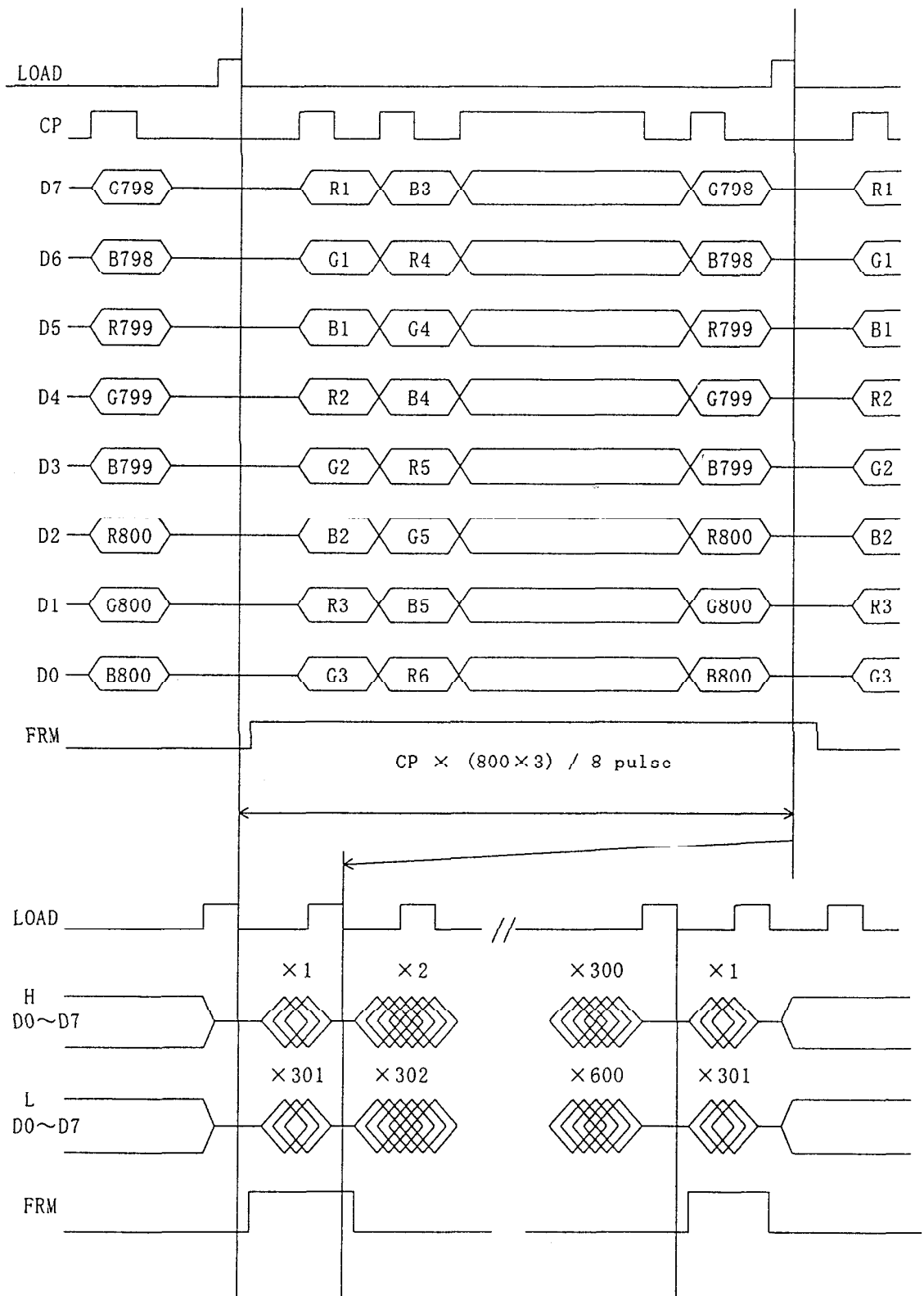
Recommended matching connector : 51021-1400(Molex)

8-2. CFL

PIN NO.	SYMBOL	DESCRIPTION	LEVEL
1	GND	Ground line (from inverter)	—
2	NC	No connect	—
3	HV	Power supply for CFL	AC

LCD side connector : QZ-19-3F01 (HONDA)
 Recommended matching connector : QZ-19-A3MYL#02 (HONDA)

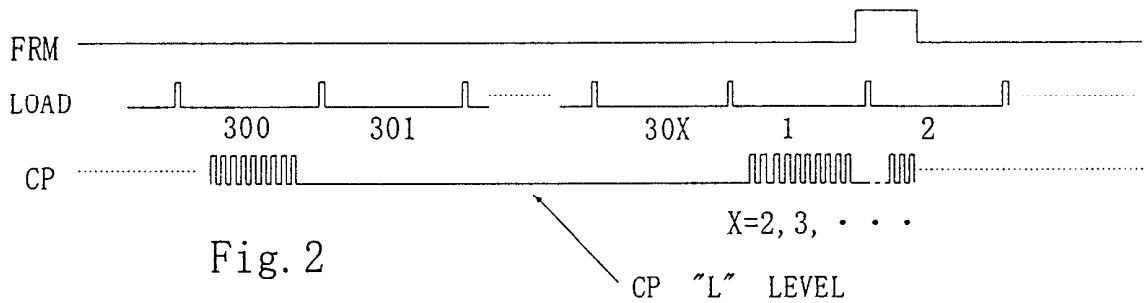
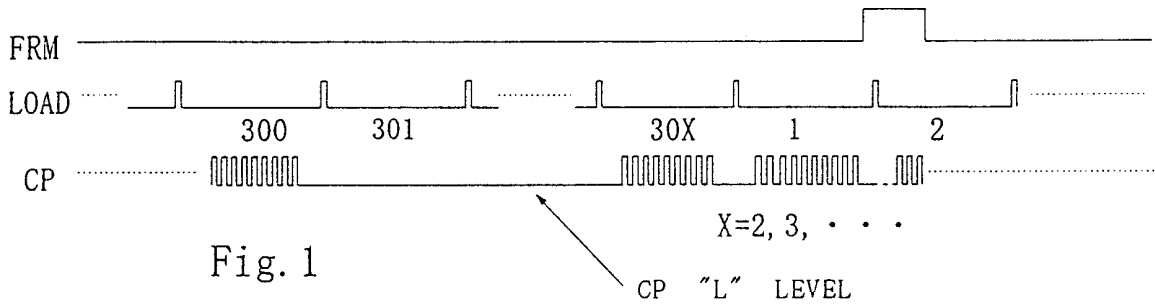
9. Interface Timing Chart



9-1. Note ~ LCD driving ~

Some graphic patterns may have a horizontal line at the center of the screen .
 Design the circuit (incl.controller) with the following conditions to minimize the center line .

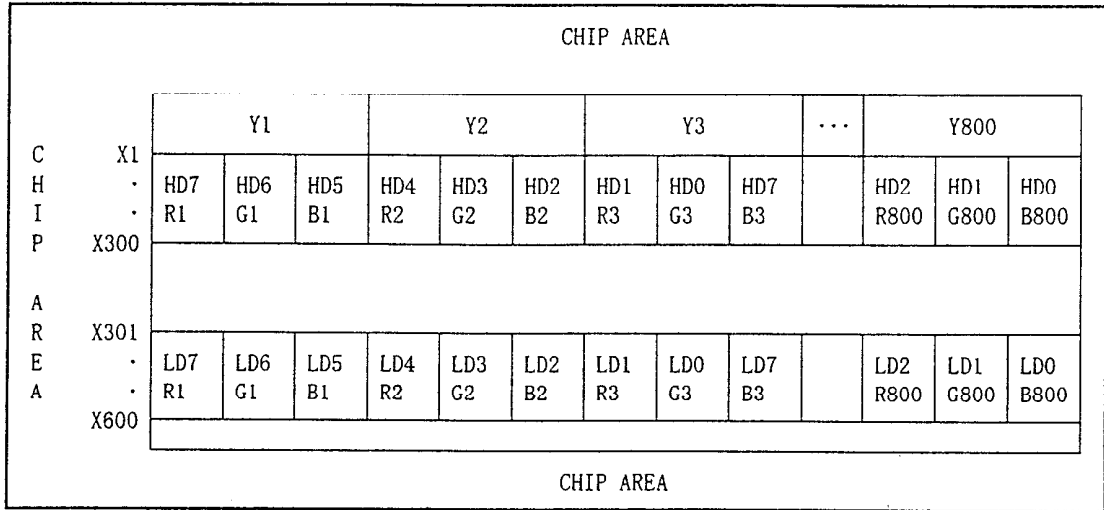
- 1) Controller duty to be 1/301 or more .
- 2) When the controller duty is 1/301 or more , interrupt CP signal while data signal is transmitted to the 301st line (and after) of each upper or lower screen .
 -See Fig.1 , Fig.2



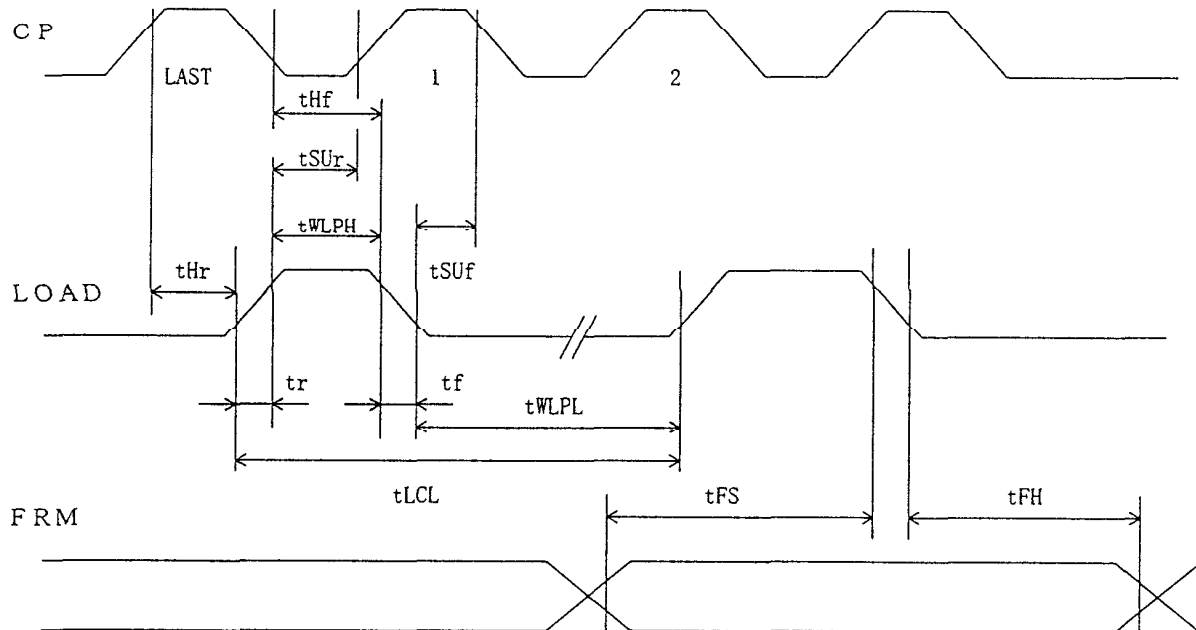
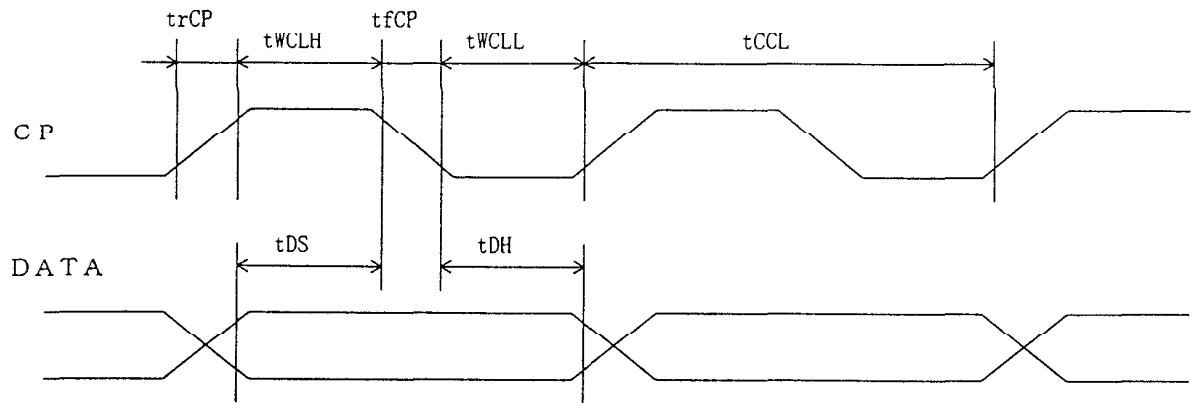
10. Data and Screen

CHIP AREA														
		Y1			Y2			Y3			...	Y800		
C H I P	X1	HD7	HD6	HD5	HD4	HD3	HD2	HD1	HD0	HD7		HD2	HD1	HD0
	X300	R1	G1	B1	R2	G2	B2	R3	G3	B3		R800	G800	B800
A R E A	X301	LD7	LD6	LD5	LD4	LD3	LD2	LD1	LD0	LD7		LD2	LD1	LD0
	X600	R1	G1	B1	R2	G2	B2	R3	G3	B3		R800	G800	B800
CHIP AREA														

10. Data and Screen



11. Input Timing Characteristics



11-1. Switching characteristics

Input characteristics : VDD = +5.0V ± 5%

Temp. = 25°C

ITEM	SYMBOL	MIN.	MAX.	UNIT
CP Cycle *1	tCCL	56	—	ns
CP "H" Pulse Width	tWCLH	15	—	ns
CP "L" Pulse Width	tWCLL	15	—	ns
CP Rise Up Time *2	trCP	—	50	ns
CP Fall Down Time *2	tfCP	—	50	ns
Data Set Up Time	tDS	10	—	ns
Data Hold Time	tDH	10	—	ns
Load "H" Pulse Width	tWLPH	100	—	ns
Load "L" Pulse Width	tWLPL	100	—	ns
LOAD Cycle	tLCL	10	—	μs
Data Strobe Set Up Time	tSUr	20	—	ns
Data Strobe Set Up Time	tSUf	20	—	ns
Data Strobe Hold Time	tHr	5	—	ns
Data Strobe Hold Time	tHf	20	—	ns
Input Signal Rise Up Time	tr	—	30	ns
Input Signal Fall Down Time	tf	—	30	ns
FRM Data Set Up Time	tFS	100	—	ns
FRM Data Hold Time	tFH	100	—	ns

*1 CP Cycle is adjust so that FRM signal is 75 Hz.

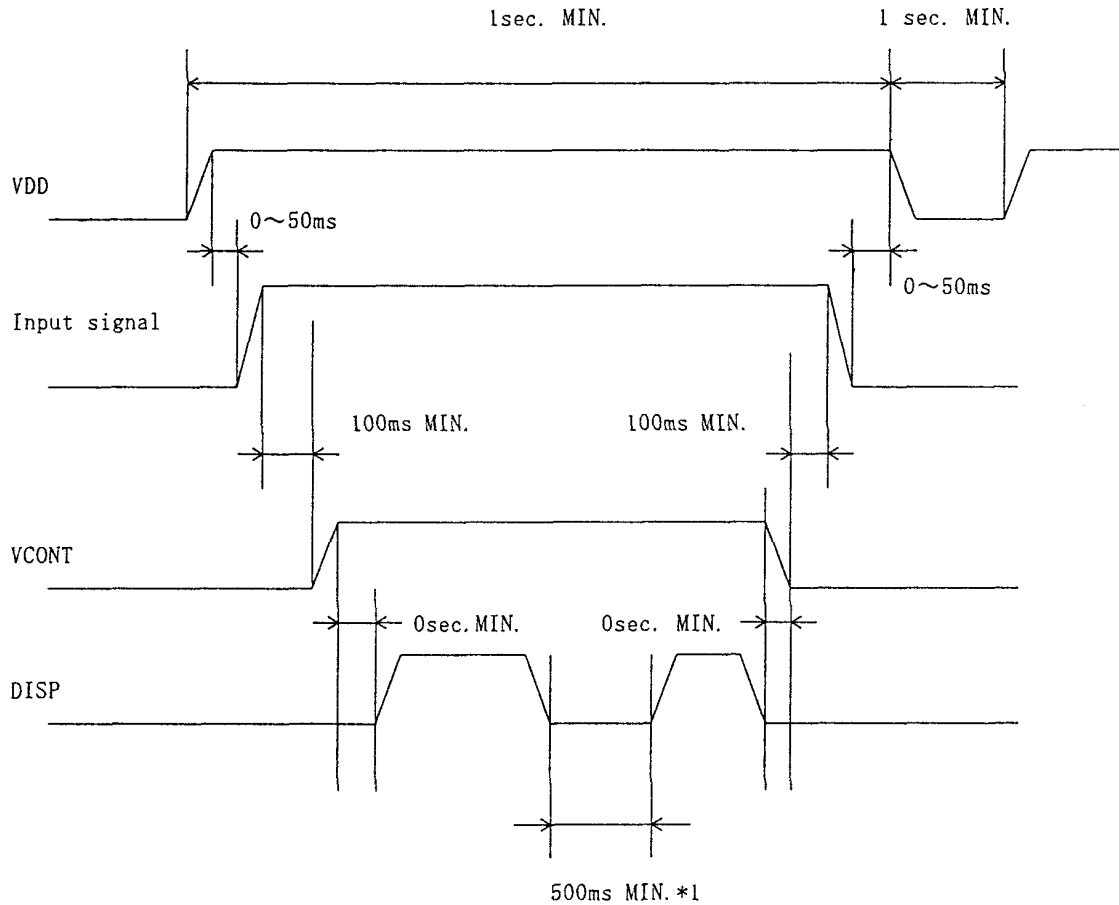
*2 The formula of condition

$$\textcircled{1} \quad tr_{CP}, tf_{CP} < \{t_{CCL} - (t_{WCLH} + t_{WCLL})\} / 2$$

Please use on condition that $\textcircled{1}$ is filled.

1 2. Supply Voltage Sequence Condition

DO NOT apply DC voltage to the LCD panel. DC voltage induce irreversible electrochemical reactions and reduce LCD life. Always follow the power supply ON/OFF sequence of VDD first, input signal second, VCONT third and finally DISP. This will prevent DC driving of the LCD or CMOS LSI latch up as shown below.



*1 Take interval time for minimum 500ms once you cut off the Disp signal.

* Control the supply voltage sequence not to float all signal line when the LCD panel is driving.

1 3 . Backlight Characteristics

CFL ratings

Measuring Inverter: PH-BLC08-K2 (HITACHI MEDIA ELECTRONICS)

Temp. = 25°C

ITEM	SYMBOL	MIN.	TYP.	MAX.	NOTE
Starting discharge Voltage *1	VS	—	—	(1270) Vrms.	0 °C
		—	—	(845) Vrms.	25 °C
Discharging tube current	IL	2.0 mArms.	5.0 mArms.	6.0 mArms.	—
Discharging tube voltage	VL	—	(520) Vrms.	—	—
Operating life *2 (IL=5.0 mArms.)	T	(25,000) Hr.	(40,000) Hr.	—	—
Operating frequency	F	(40) kHz	—	(100) kHz	—

*1 The Non-load output voltage (VS) of the inverter should be designed to have some margin, because VS may increase due to the leak current which may be caused by wiring of CFL cables. (Reference value : (1.650) Vrms MIN.)

*2 When the illuminance or quantity of light has decreased to 50 % of the initial value.

* Do not apply more than 6.0mA discharge tube current . Because CFL maybe broken due to over current.

1 4. Lot Number Identification

The lot number shall be indicated on the back of the backlight case of each LCD.

KHB104SV1AA-G81- □ □-□ □



YEAR	1999	2000	2001	2002	2003	2004
CODE	9	0	1	2	3	4

MONTH	JAN.	FEB.	MAR.	APR.	MAY	JUN.
CODE	1	2	3	4	5	6

MONTH	JUL.	AUG.	SEP.	OCT.	NOV.	DEC.
CODE	7	8	9	X	Y	Z

1 5. Warranty

15-1. Incoming inspection

Please inspect the LCD within one month after your receipt.

15-2. Production Warranty

Kyocera warrants its LCDs for a period of 12 months after receipt by the purchaser, and within the limits specified. Kyocera shall, by mutual agreement, replace or rework defective LCDs that are shown to be Kyocera's responsibility.

16. Precautions for use

16-1. Installation of the LCD

1. Please ground either of the mounting (screw) holes located at each corner of an LCD module, in order to stabilize brightness and display quality.
2. A transparent protection plate shall be added to protect the LCD and its polarizers.
3. The LCD shall be installed so that there is no pressure on the LSI chips.
4. The LCD shall be installed flat, without twisting or bending.
5. The display window size should be the same as the effective viewing area.
6. In case you use outside frame of effective viewing area as outward appearance of your product, unevenness of its outward appearance is out of guarantee.
7. Do not pull the CFL lead wires and do not bend the root of the wires. Housing should be designed to protect CFL lead wires from external stress.

16-2. Static Electricity

1. Since CMOS ICs are mounted directly onto the LCD glass, protection from static electricity is required. Operation should wear ground straps.

16-3. LCD Operation

1. The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.
2. Vop must be adjusted to optimize viewing angle and contrast.
3. Operation of the LCD at temperature below the limit specified may cause image degradation and/or bubbles. It may also change the characteristics of the liquid crystal. This phenomenon may not recover. The LCD shall be operated within the temperature limits specified.

16-4. Storage

1. The LCD shall be stored within the temperature and humidity limits specified. Store in a dark area, and protected the LCD from direct sunlight or fluorescent light.
2. The LCD should be packaged to prevent damage.

16-5. Screen Surface

1. DO NOT store in a high humidity environment for extended periods. Image degradation, bubbles, and/or peeling off of polarizer may result.
2. The front polarizer is easily scratched or damaged. Prevent touching it with any hard material, and from being pushed or rubbed.
3. The LCD screen may be cleaned with a soft cloth or cotton pad. Methanol, or Isopropyl Alcohol may be used, but insure that all solvent residue is removed.
4. Water may cause damage or discoloration of the polarizer. Clean any condensation or moisture from any source immediately.
5. Always keep the LCD free from condensation during testing. Condensation may permanently spot or stain the polarizers.

17. Reliability Data / Environmental Test

TEST ITEM	TEST CONDITION	TEST TIME	RESULT
High Temp. Atmosphere	70°C	240 Hr.	Display Quality : No defect Display Function : No defect Current Consumption : No defect
Low Temp. Atmosphere	-20°C	240 Hr.	Low Temp. Bubble : None Solid Crystallization of Liquid Crystal : None Display Quality : No defect Display Function : No defect Current Consumption : No defect
High Temp. Humidity Atmosphere	40°C 90%RH	240 Hr.	Display Quality : No defect Display Function : No defect Peel-off of Organic Sealing : None Current Consumption : No defect
Temp. Cycle	-20°C 0.5 Hr. R. T. 0.5 Hr. 70°C 0.5 Hr.	10cycles	Display Quality : No defect Display Function : No defect Peel-off of Organic Sealing : None Bubble on Cell : None
High Temp. Operation	50°C Yop	500 Hr.	Display Quality : No defect Current Consumption : No defect

* Each test item uses a test LCD only once. The tested LCD is not used in any other tests.

* The LCD is tested in circumstances in which there is no condensation.

* The tested LCD is inspected after 24 hours of storage at room temperature and room humidity after each test is finished.

* The reliability test is not an out-going inspection.

* The results of the reliability test are for your reference purpose only. The reliability test is conducted only to examine the LCD's capability.

Document NO.	TQ3C-8EAC0-E2AAZS19-00
DATE	September 1, 1999

FOR: _____

KYOCERA INSPECTION STANDARD

TYPE : KHB104SV1AA-G81

KYOCERA CORPORATION
KAGOSHIMA HAYATO PLANT
LCD DIVISION

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Revision Record

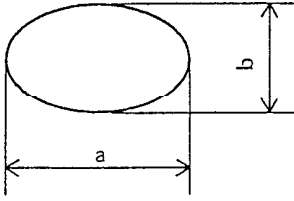
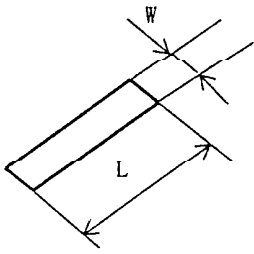
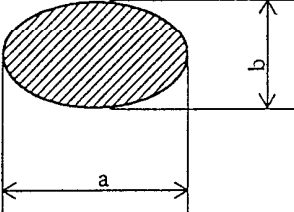
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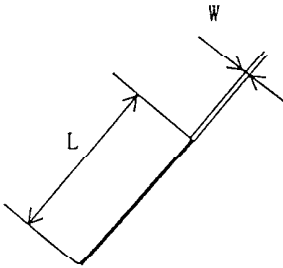
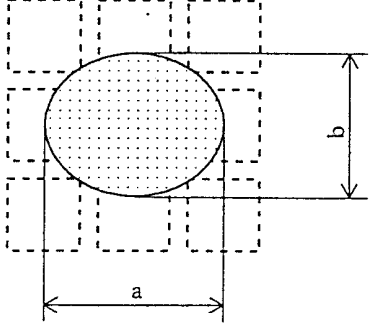
Visuals specification

1)Note

Item	Note	
General	<p>1. When defects specified in this Inspection Standards are inspected, operating voltage (Vop) shall be set at the level where optimized contrast is available. Display quality is applied up to effective viewing area. (Bi-Level INSPECTION)</p> <p>2. This inspection standard about the image quality shall be applied to any defect within the effective viewing area and shall not be applicable to outside of the area.</p> <p>3. Should any defects which are not specified in this standard happen, additional standard shall be determined by mutual agreement between customer and Kyocera.</p> <p>4. Inspection conditions</p> <p>Luminance : 500 Lux minimum . Inspection distance : 300 mm (from the sample) Temperature : 25 ± 5 °C Direction : right above</p>	
Definition of Inspection item	Pinhole, Bright spot Black spot, Scratch Foreign particle	The color of a small area is different from the remainder. The phenomenon dose not change with voltage.
	Contrast variation	The color of a small area is different from the remainder. The phenomenon changes with voltage.
	Polarizer (Scratch, Bubble, Dent)	Scratch, Bubble and Dent in the polarizer which can be observed in on / off state.

2) Standard

Inspection item	Judgement standard																						
Pinhole, Bright spot Black spot, Foreign particle	<div style="text-align: center;">  $d = (a + b) / 2$ <table border="1" data-bbox="657 493 1412 714"> <thead> <tr> <th>Category</th> <th>Size (mm)</th> <th>Acceptable number</th> </tr> </thead> <tbody> <tr> <td>A</td> <td>$d \leq 0.2$</td> <td>neglected</td> </tr> <tr> <td>B</td> <td>$0.2 < d \leq 0.3$</td> <td>5</td> </tr> <tr> <td>C</td> <td>$0.3 < d \leq 0.5$</td> <td>3</td> </tr> <tr> <td>D</td> <td>$0.5 < d$</td> <td>0</td> </tr> </tbody> </table> </div>	Category	Size (mm)	Acceptable number	A	$d \leq 0.2$	neglected	B	$0.2 < d \leq 0.3$	5	C	$0.3 < d \leq 0.5$	3	D	$0.5 < d$	0							
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Scratch, Foreign particle	<div style="text-align: center;">  <table border="1" data-bbox="630 1039 1442 1333"> <thead> <tr> <th></th> <th>Width (mm)</th> <th>Length (mm)</th> <th>Acceptable No.</th> </tr> </thead> <tbody> <tr> <td>A</td> <td>$W \leq 0.03$</td> <td>—</td> <td>neglected</td> </tr> <tr> <td>B</td> <td rowspan="3">$0.03 < W \leq 0.1$</td> <td>$L \leq 2.0$</td> <td>neglected</td> </tr> <tr> <td>C</td> <td>$2.0 < L \leq 4.0$</td> <td>3</td> </tr> <tr> <td>D</td> <td>$4.0 < L$</td> <td>0</td> </tr> <tr> <td>E</td> <td>$0.1 < W$</td> <td>—</td> <td>According to 'Circular'</td> </tr> </tbody> </table> </div>		Width (mm)	Length (mm)	Acceptable No.	A	$W \leq 0.03$	—	neglected	B	$0.03 < W \leq 0.1$	$L \leq 2.0$	neglected	C	$2.0 < L \leq 4.0$	3	D	$4.0 < L$	0	E	$0.1 < W$	—	According to 'Circular'
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Polarizer (Scratch, Bubble, Dent)	<p data-bbox="613 191 768 218">(1) Scratch</p>  <table border="1" data-bbox="621 611 1430 926"> <thead> <tr> <th></th> <th>Width (mm)</th> <th>Length (mm)</th> <th>Acceptable No.</th> </tr> </thead> <tbody> <tr> <td>A</td> <td>$W \leq 0.1$</td> <td>—</td> <td>neglected</td> </tr> <tr> <td>B</td> <td rowspan="2">$0.1 < W \leq 0.3$</td> <td>$L \leq 5.0$</td> <td>neglected</td> </tr> <tr> <td>C</td> <td>$5.0 < L$</td> <td>0</td> </tr> <tr> <td>D</td> <td>$0.3 < W$</td> <td>—</td> <td>0</td> </tr> </tbody> </table>		Width (mm)	Length (mm)	Acceptable No.	A	$W \leq 0.1$	—	neglected	B	$0.1 < W \leq 0.3$	$L \leq 5.0$	neglected	C	$5.0 < L$	0	D	$0.3 < W$	—	0
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	<p data-bbox="613 974 854 1001">(2)Bubble (dent)</p>  <p data-bbox="1062 1472 1300 1499">$d = (a + b) / 2$</p> <table border="1" data-bbox="643 1545 1398 1860"> <thead> <tr> <th>Category</th> <th>Size (mm)</th> <th>Acceptable number</th> </tr> </thead> <tbody> <tr> <td>A</td> <td>$d \leq 0.2$</td> <td>neglected</td> </tr> <tr> <td>B</td> <td>$0.2 < d \leq 0.3$</td> <td>5</td> </tr> <tr> <td>C</td> <td>$0.3 < d \leq 0.5$</td> <td>3</td> </tr> <tr> <td>D</td> <td>$0.5 < d$</td> <td>0</td> </tr> </tbody> </table>	Category	Size (mm)	Acceptable number	A	$d \leq 0.2$	neglected	B	$0.2 < d \leq 0.3$	5	C	$0.3 < d \leq 0.5$	3	D	$0.5 < d$	0				
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